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MS-A9281

Ver: 1.1

CPU:

INTEL - Celeron 847

System Chipset:

INTEL - NM70

OnBoard Chipset:

HD Audio Codec:ALC887 CG

LAN:Realtek RTL8111E CG

SIO:FINTEK F71808AU

Main Memory:

DDRIII (800/1066MHz) * 1 (one Channel)

Expansion Slots:

MINIPCI Express (X1) Slot * 2

PWM:

Controller:ISL95837HRZ (17W)

Other:

SATA(SATA2-300MB/s) *2

USB2.0 *4 (Rear X 2 & Side X 2)

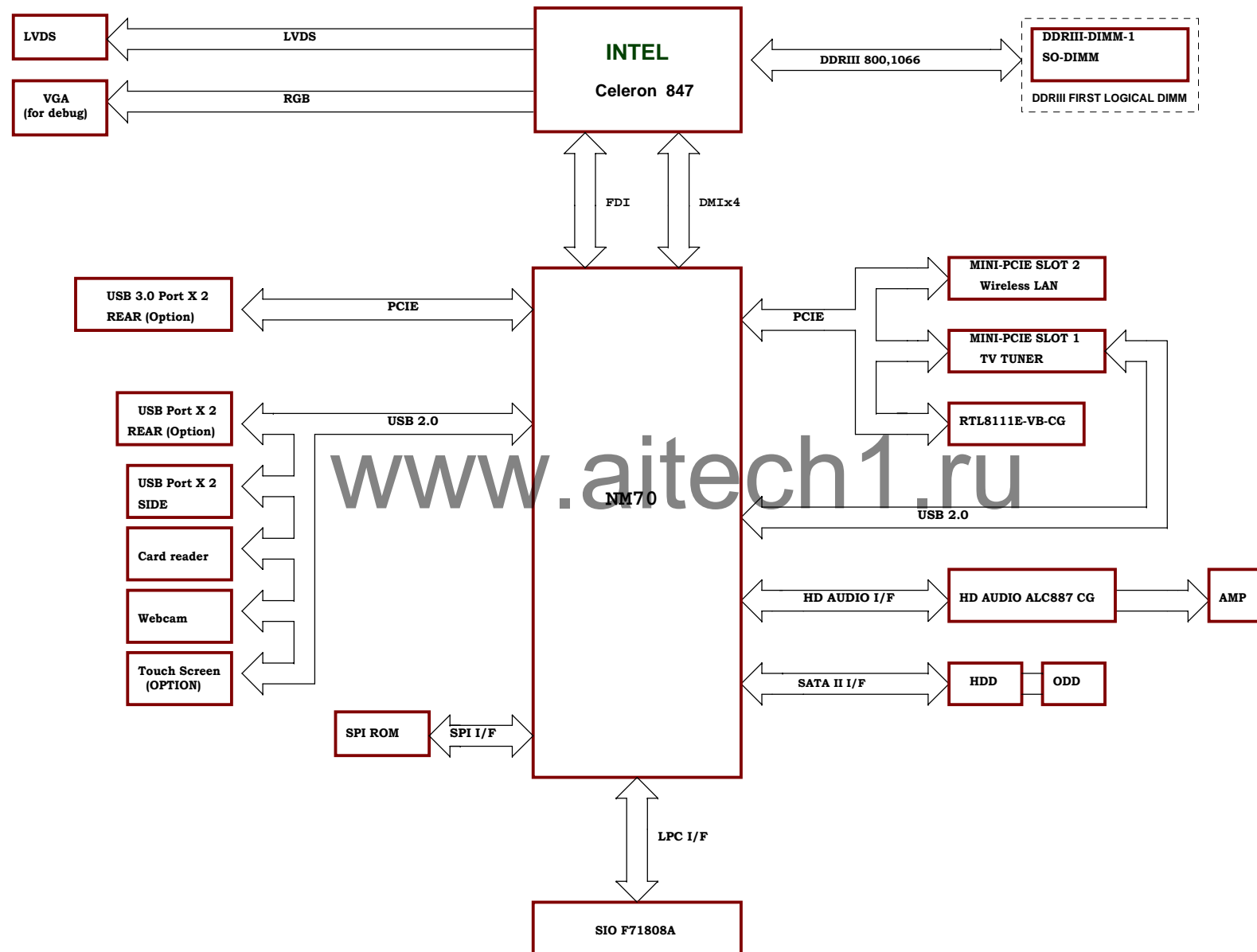
Card Reader (JUSB)

Webcam (JCAMERA1)

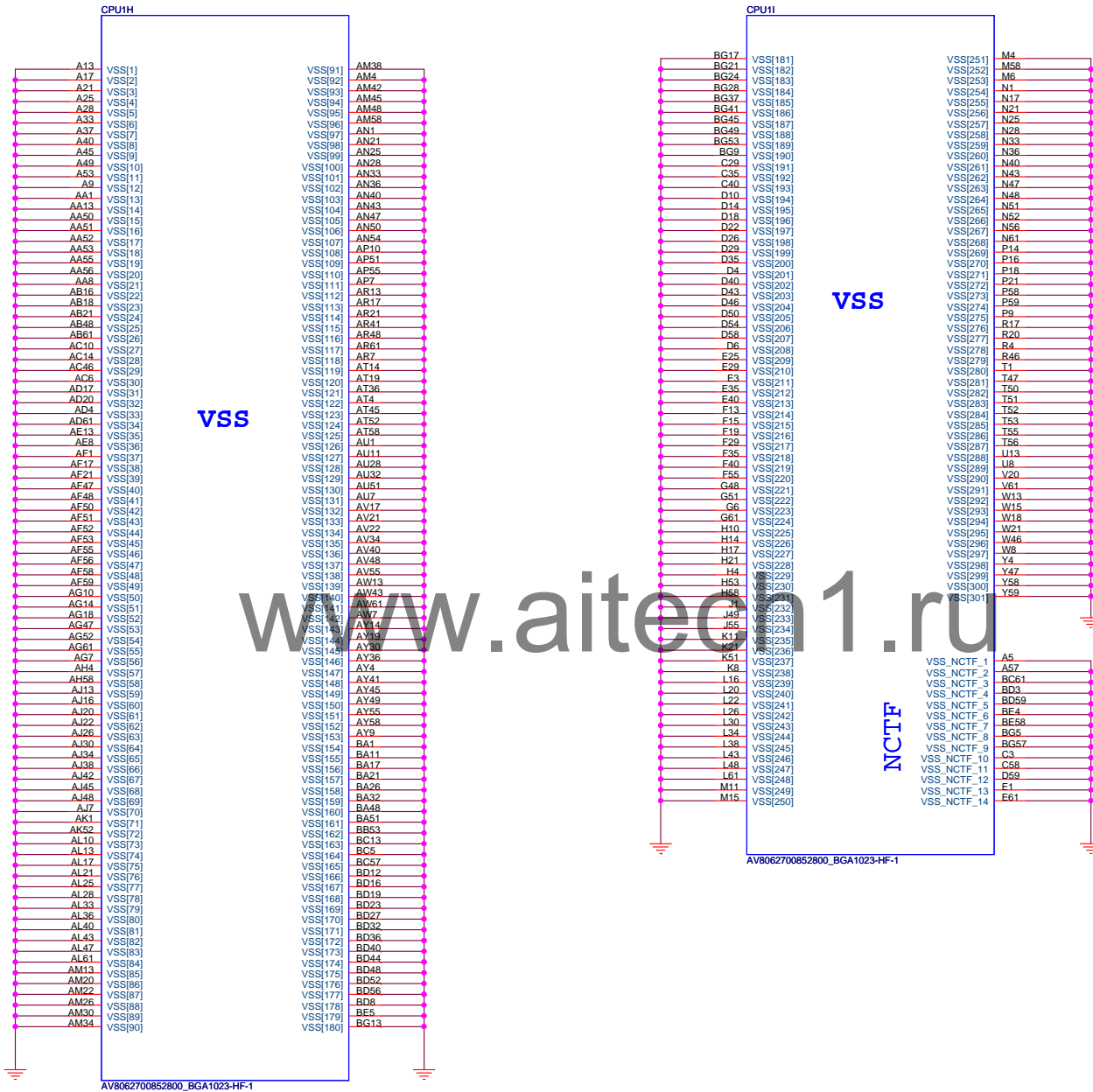


MICRO-STAR INT'L CO.,LTD			
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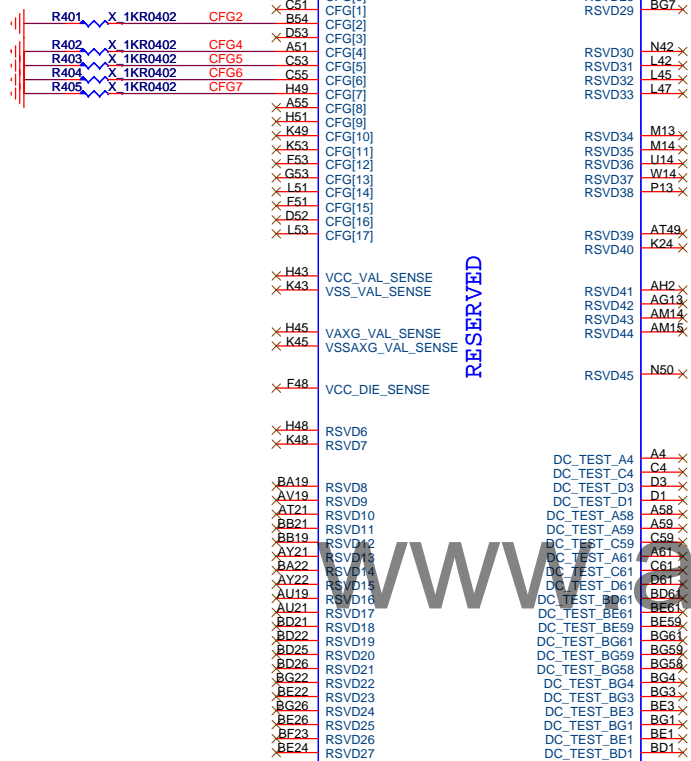
MS-A9281 1.0







Pull low for PCIe16 lane reversal.



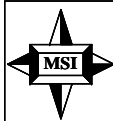
AV8062700852800_BGA1023-HF-1

CFG2 - PCI-Express Static Lane Reversal	
CFG2	1:Normal Operation 0:Lane Numbers Reversed 15 -> 0, 14 -> 1, ...

CFG4 - Display Port Presence	
CFG4	1:Disabled; No Physical Display Port attached to Embedded Display Port 0:Enabled; An external Display Port device is connected to the Embedded Display Port

PCI-Express Configuration Select	
CFG[5:6]	11:Default X16-device 1 functions 1 and 2 disabled 10: X8 X8-device 1 functions 1 enable, function2 disabled 01:Reserved--(device 1 functions 1disabled function2 enable 00: X8 X4 X4-device 1 functions 1 and 2 enable

PEG DEFER TRAINING	
CFG7	1 : (Default)PEG train immediately following xxRESETB de assertion 0 :PEG wait for BIOS for training

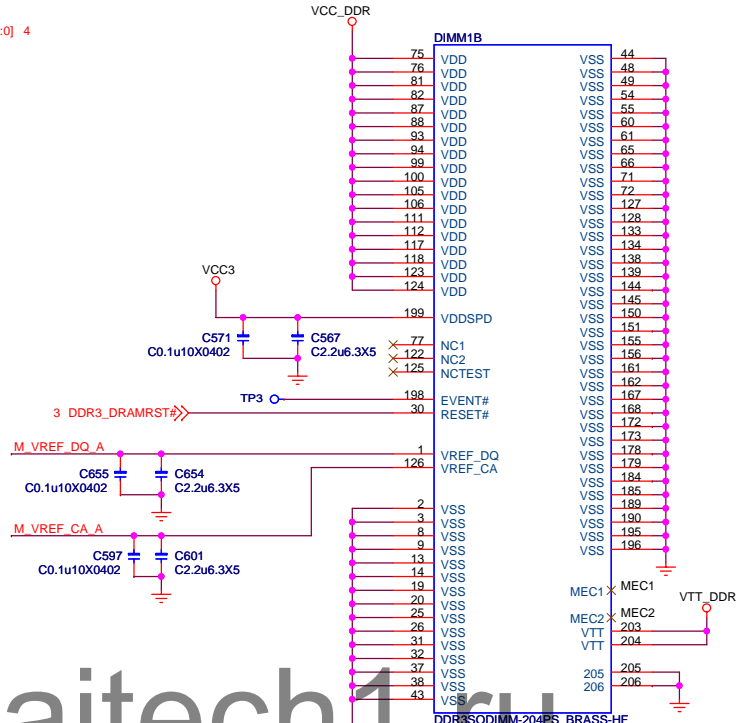
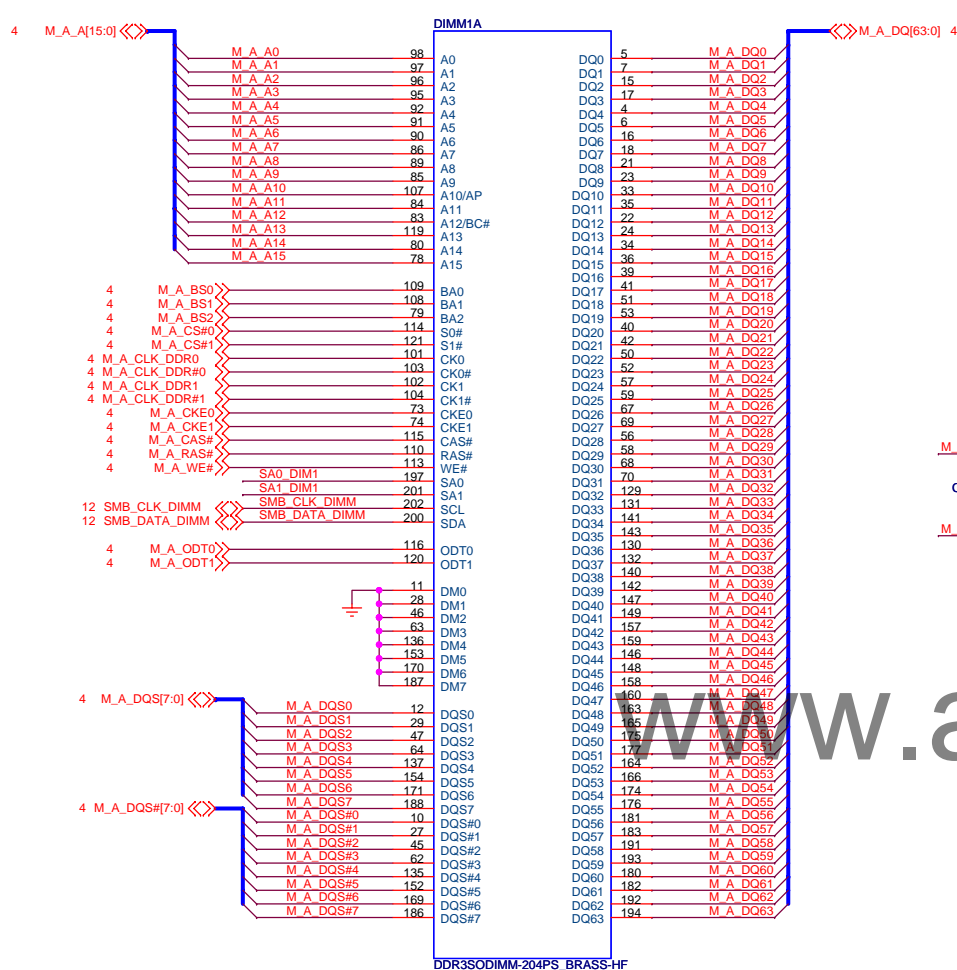


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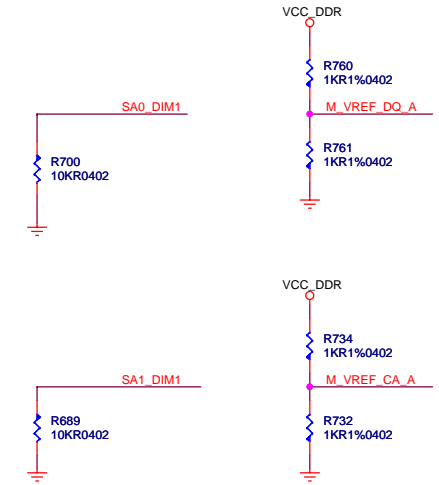
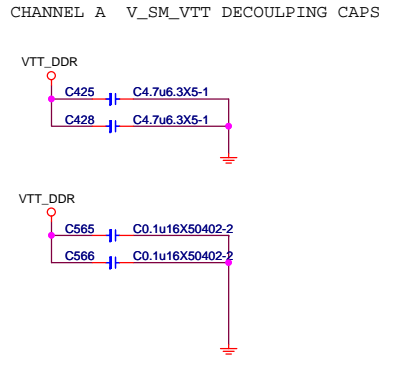
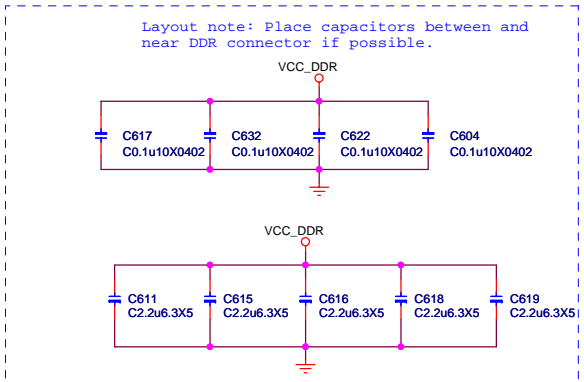
MS-A9281

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Custom	PROCESSOR-6 (RESERVE)	1.1
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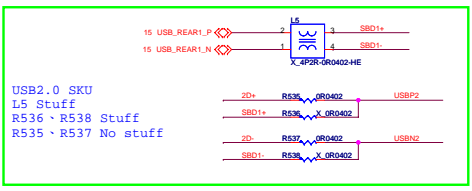
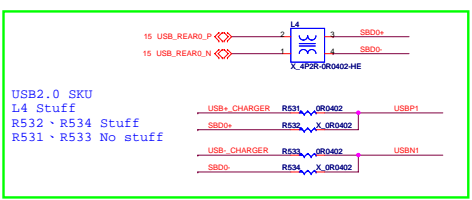
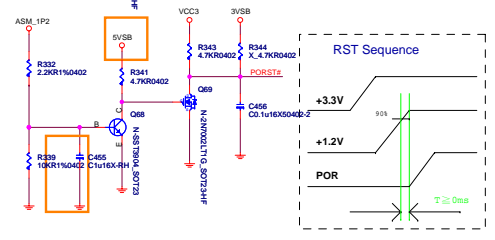
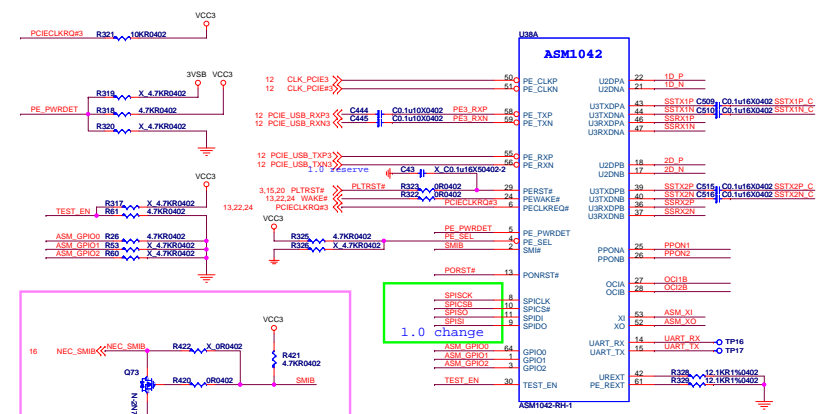
SODIMM#A



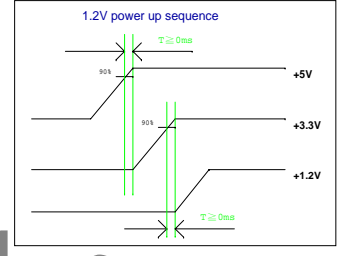
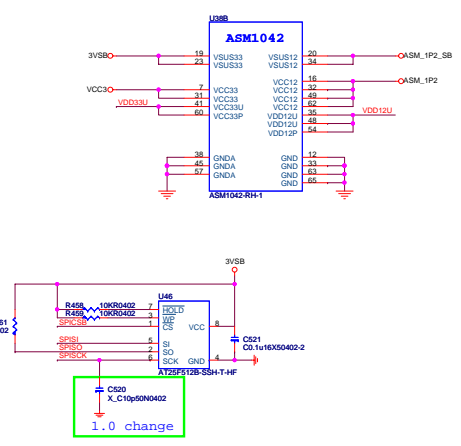
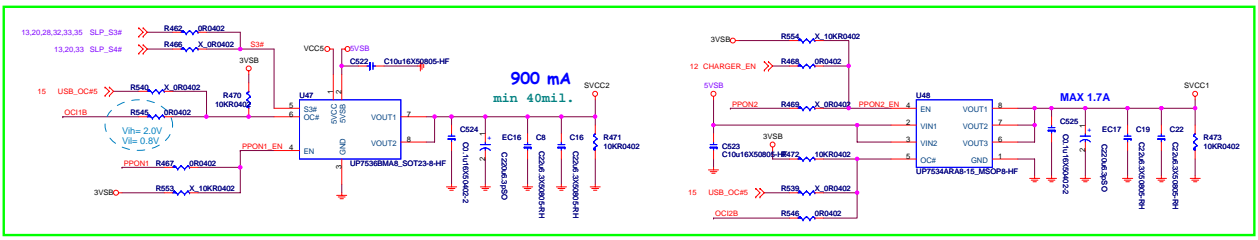
ADDRESS: 000
0xA0



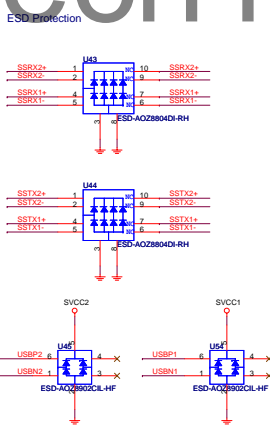
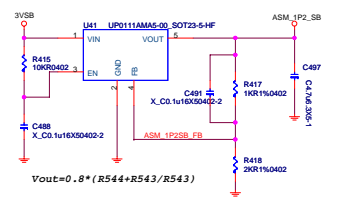
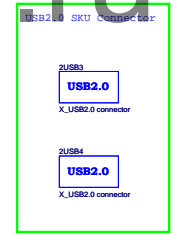
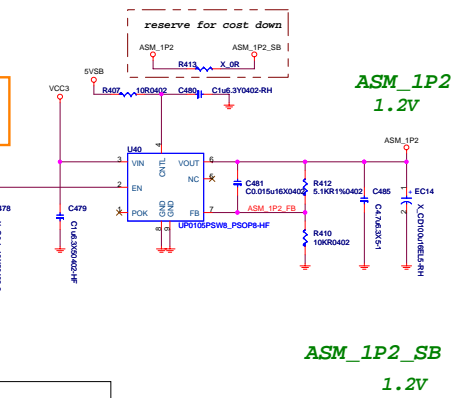
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MS-A9281		
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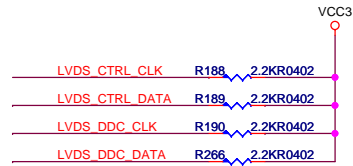


USB3.0 & USB2.0 SKU POWER KEEP

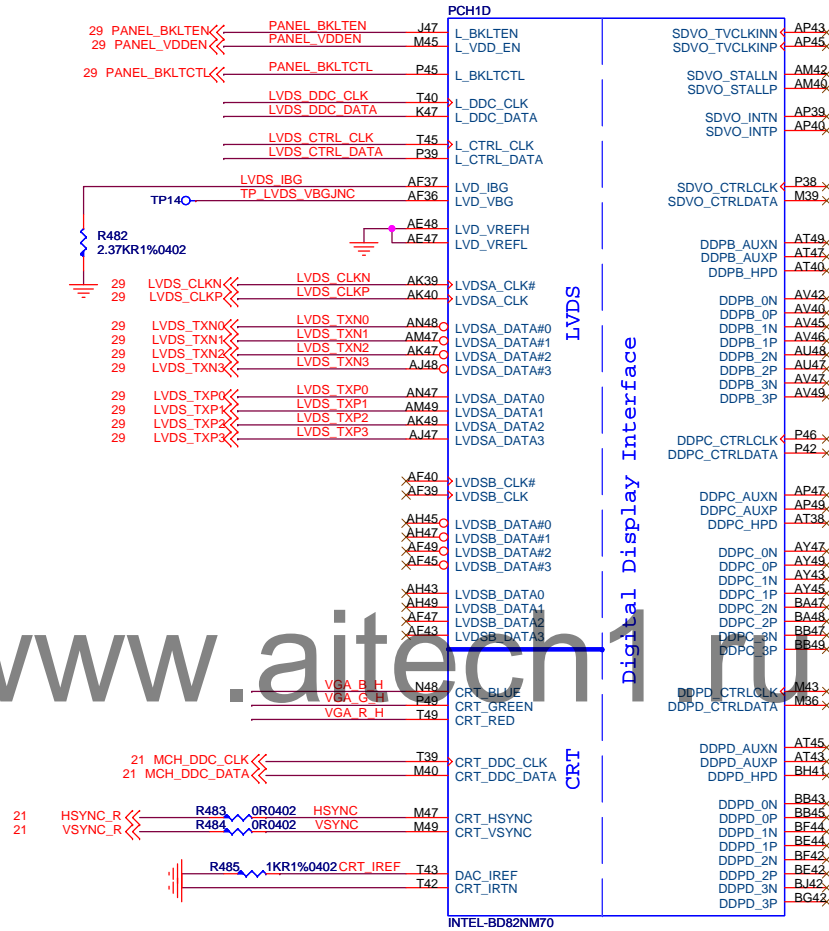
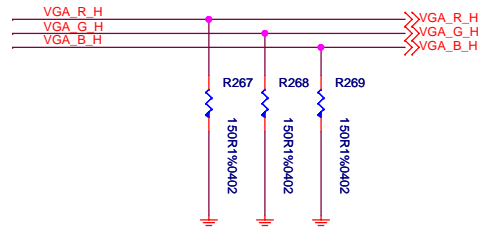


ASM1042 core Power (ASM_IP2+ASM_IP2_SB < 150mA)





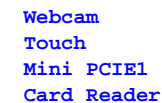
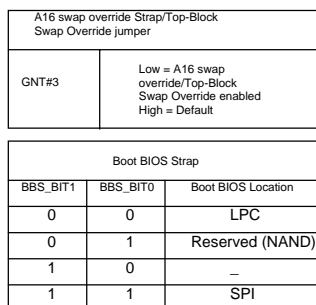
close to PCH



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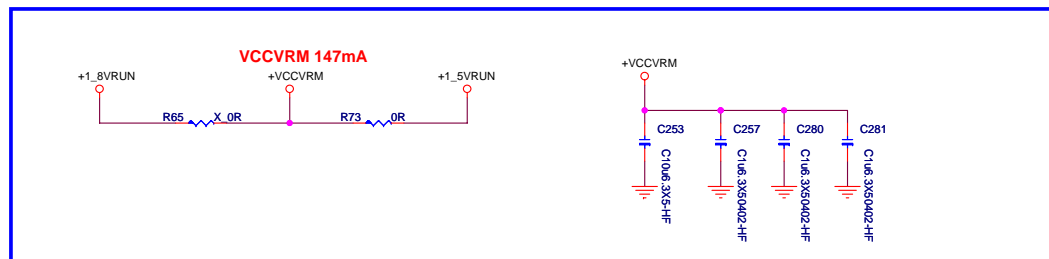
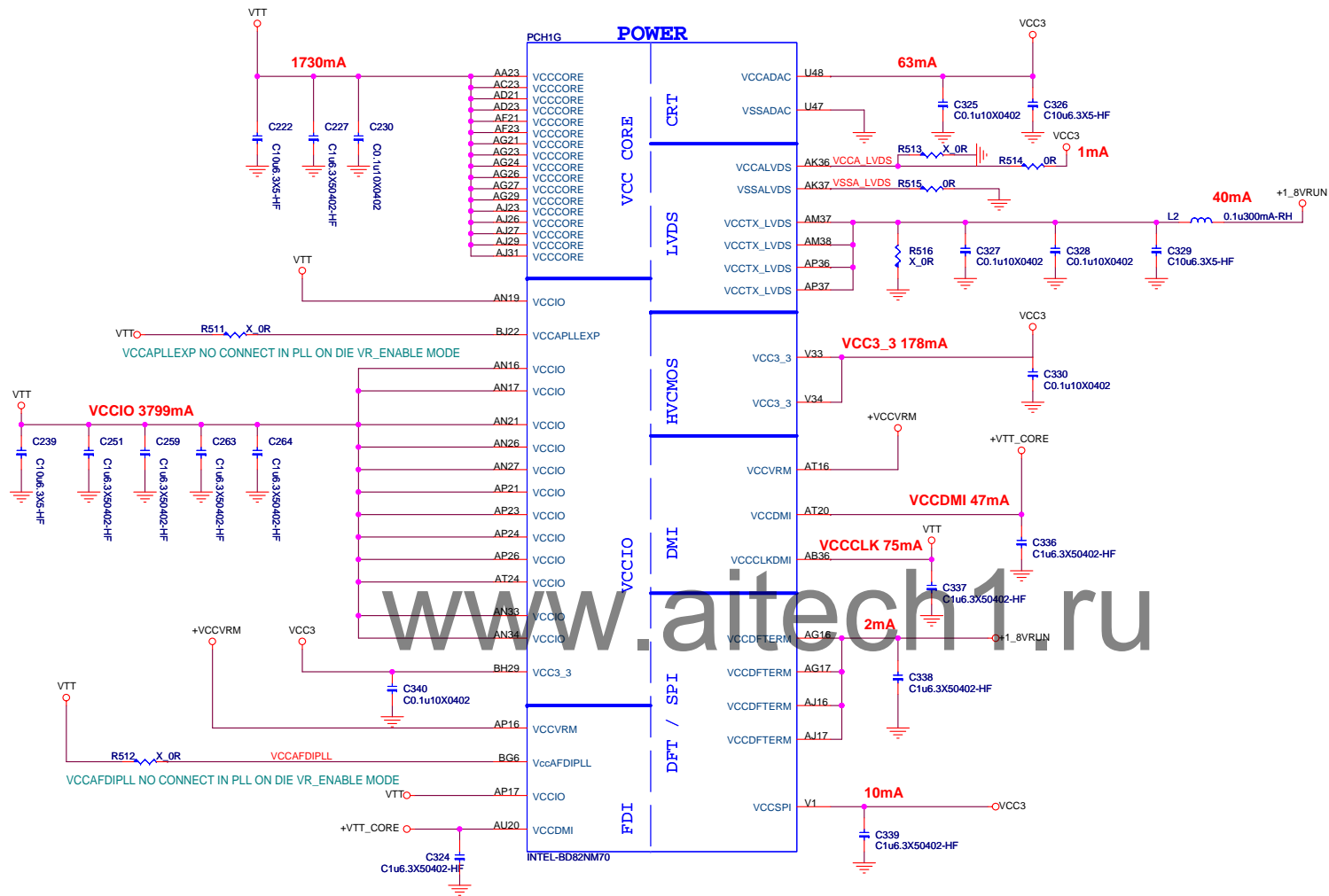
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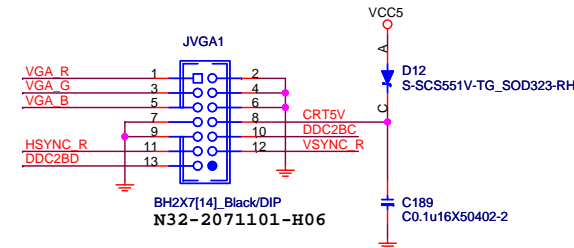
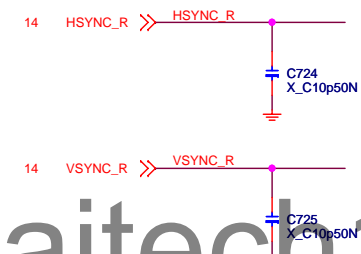
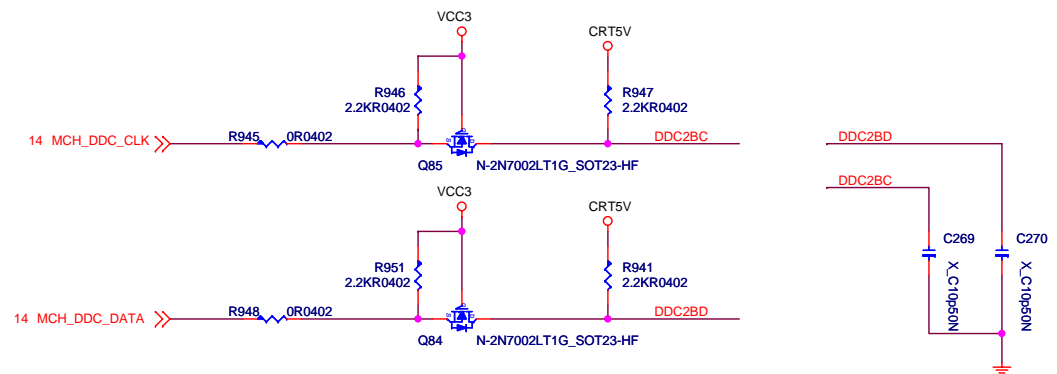
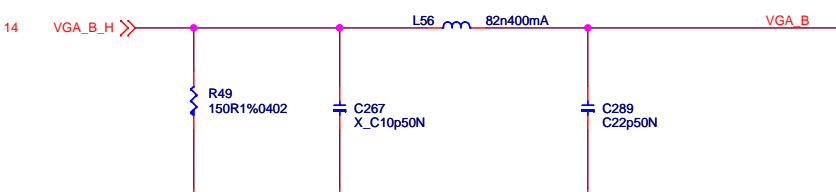
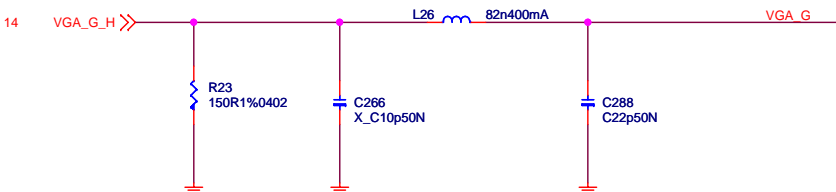
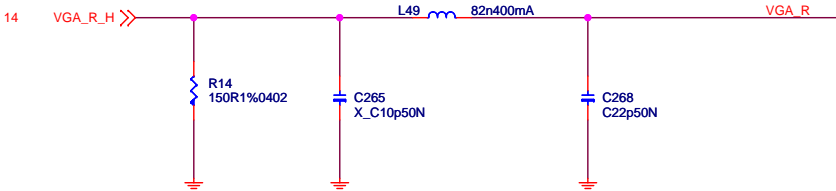
USB ports 4, 5, 6, 7 are disabled on 8 port SKUs.

Side USB1
Side USB2
Rear USB1
Rear USB2

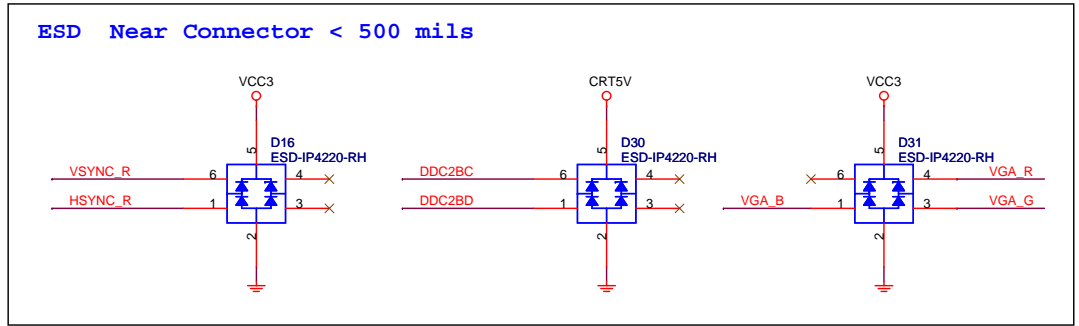
✗ USB ports 12 , 13 are disabled on 8 port SKUs.



VGA Output for Debug williy

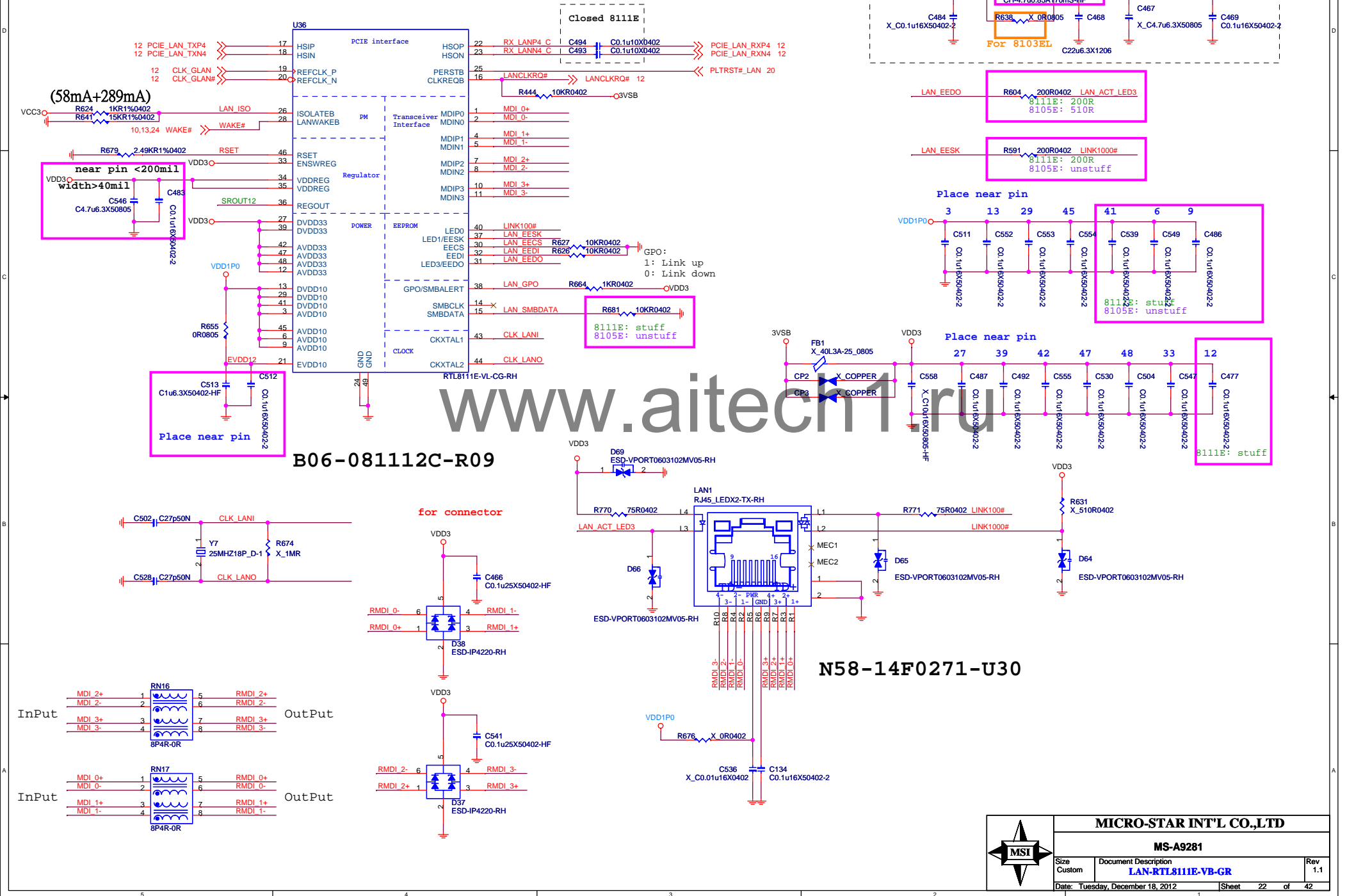


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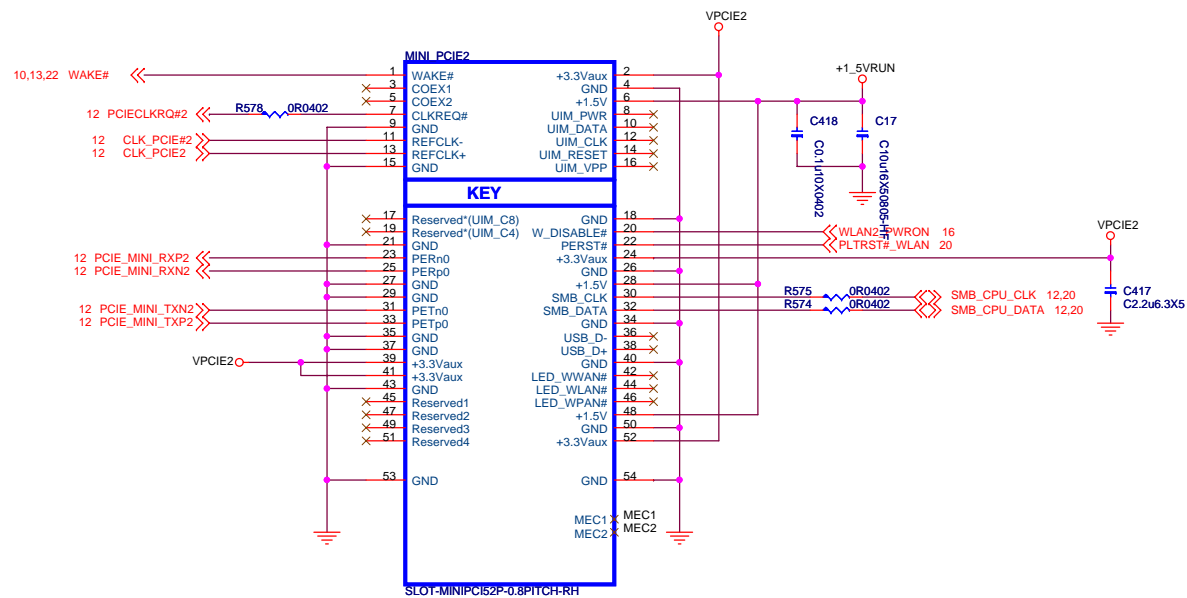


RTL8111E Giga LAN

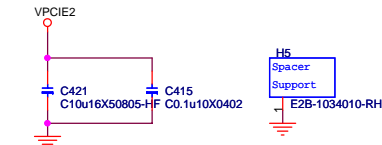
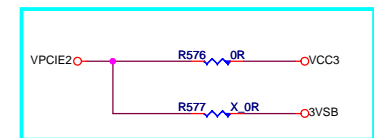
RTL8105E 10/100M LAN



Wireless LAN(Short Card)

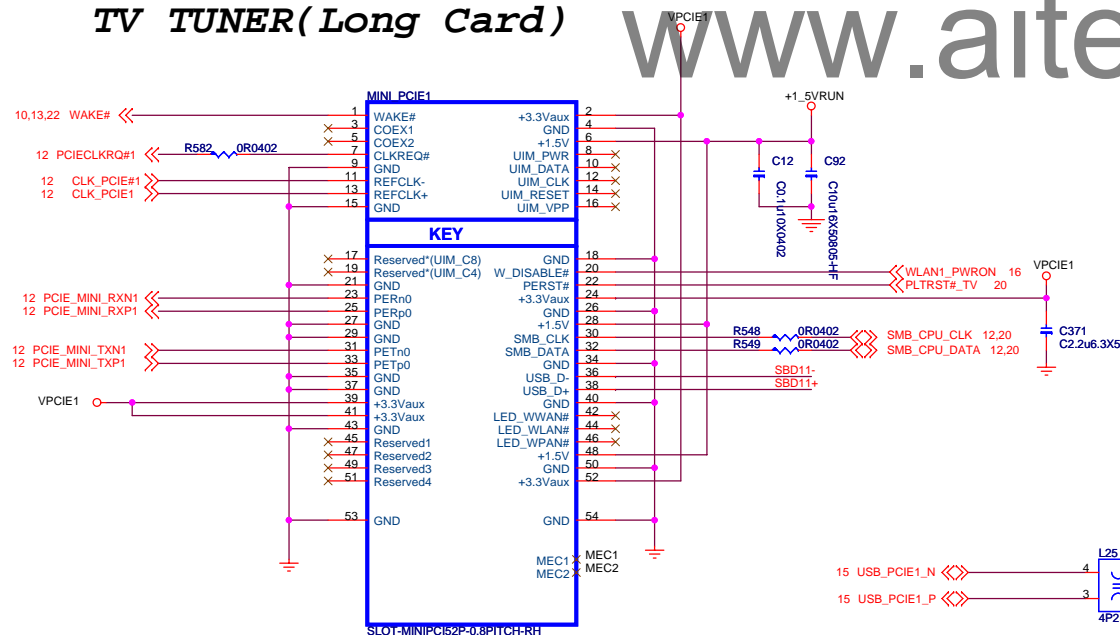


VCC3, 1.5A
+1_5VRUN, 0.5A

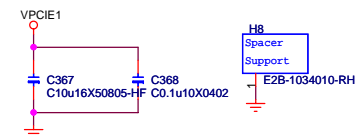
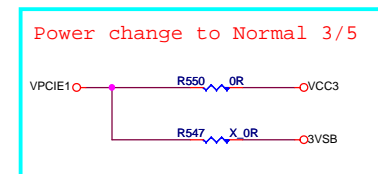


TV TUNER(Long Card)

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VCC3, 1.5A
+1_5VRUN, 0.5A



N11-0520040-A81

PCI ExpressR
Mini Card Electromechanical
Specification
Revision 1.2

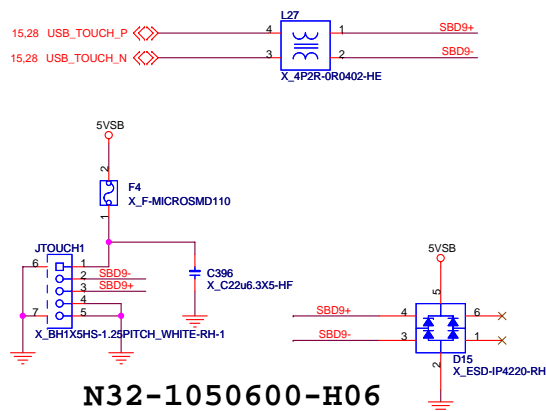


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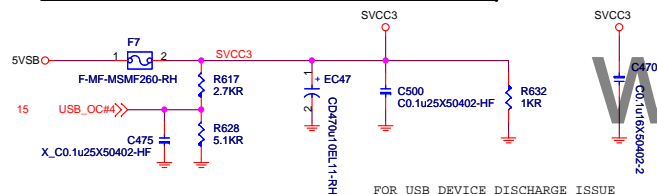
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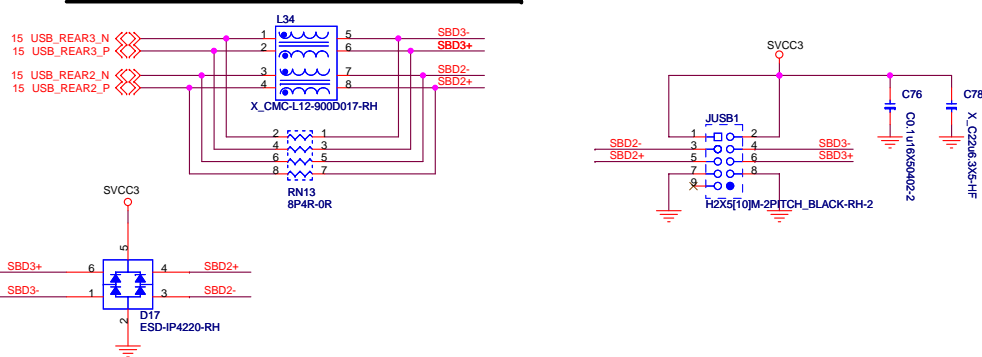
Multi Touch (Option)



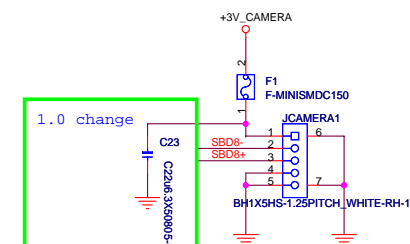
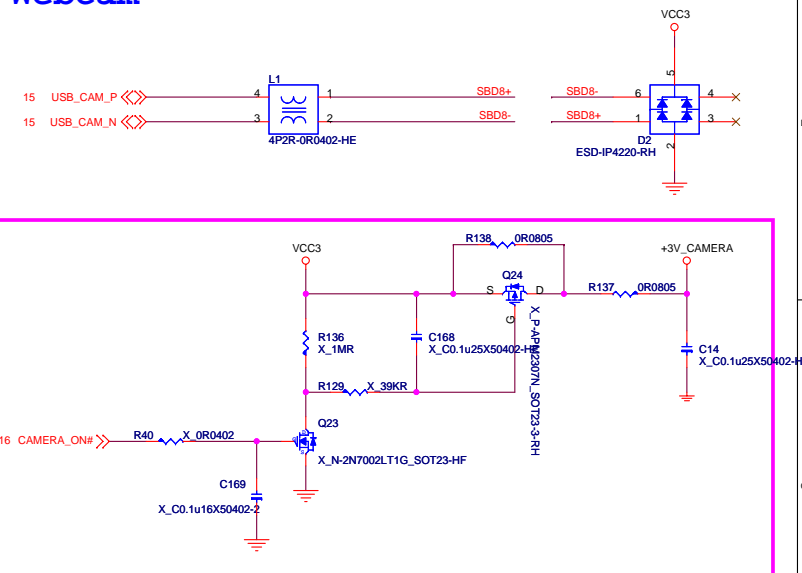
POWER CIRCUIT FOR USB PORT 2,3 (REAR)



SIDE PANEL USB CONNECTOR FOR USB PORT 2,3

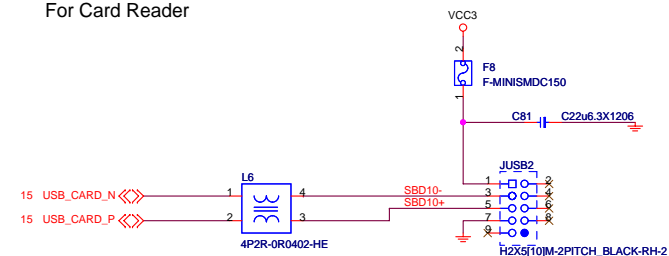


Webcam



N32-1050600-H06

For Card Reader

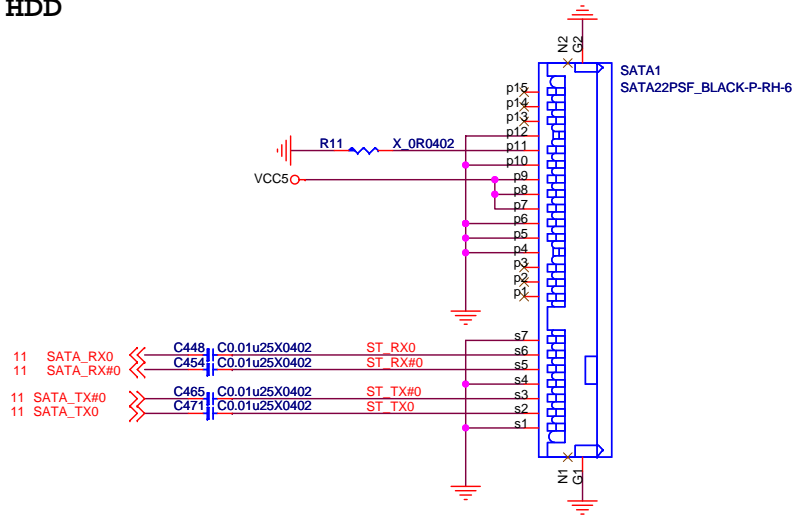


MICRO-STAR INT'L CO.,LTD

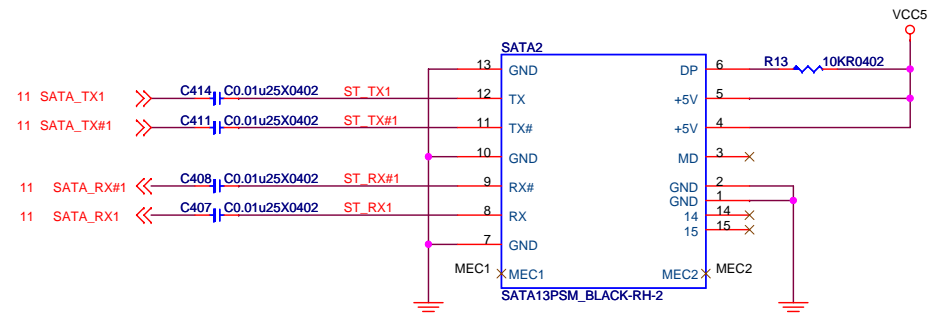
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Custom	USB / IR / TOUCH / WEBCAM	1.1
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SATA HDD



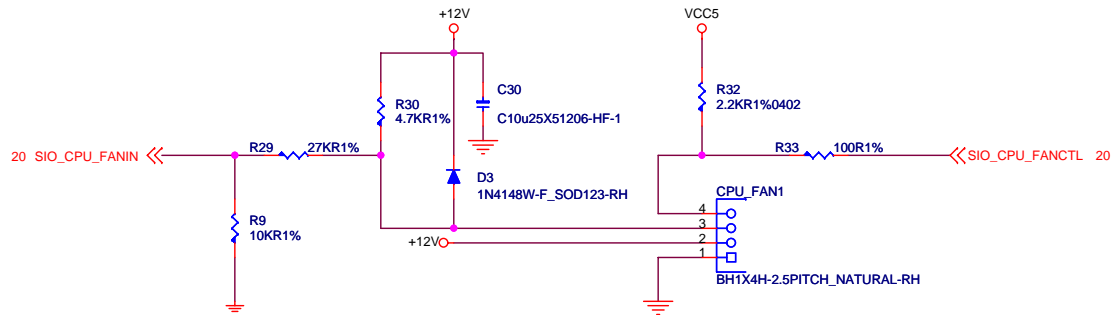
SATA ODD



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1.0 Add

CPU FAN

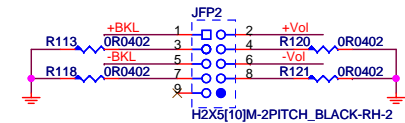
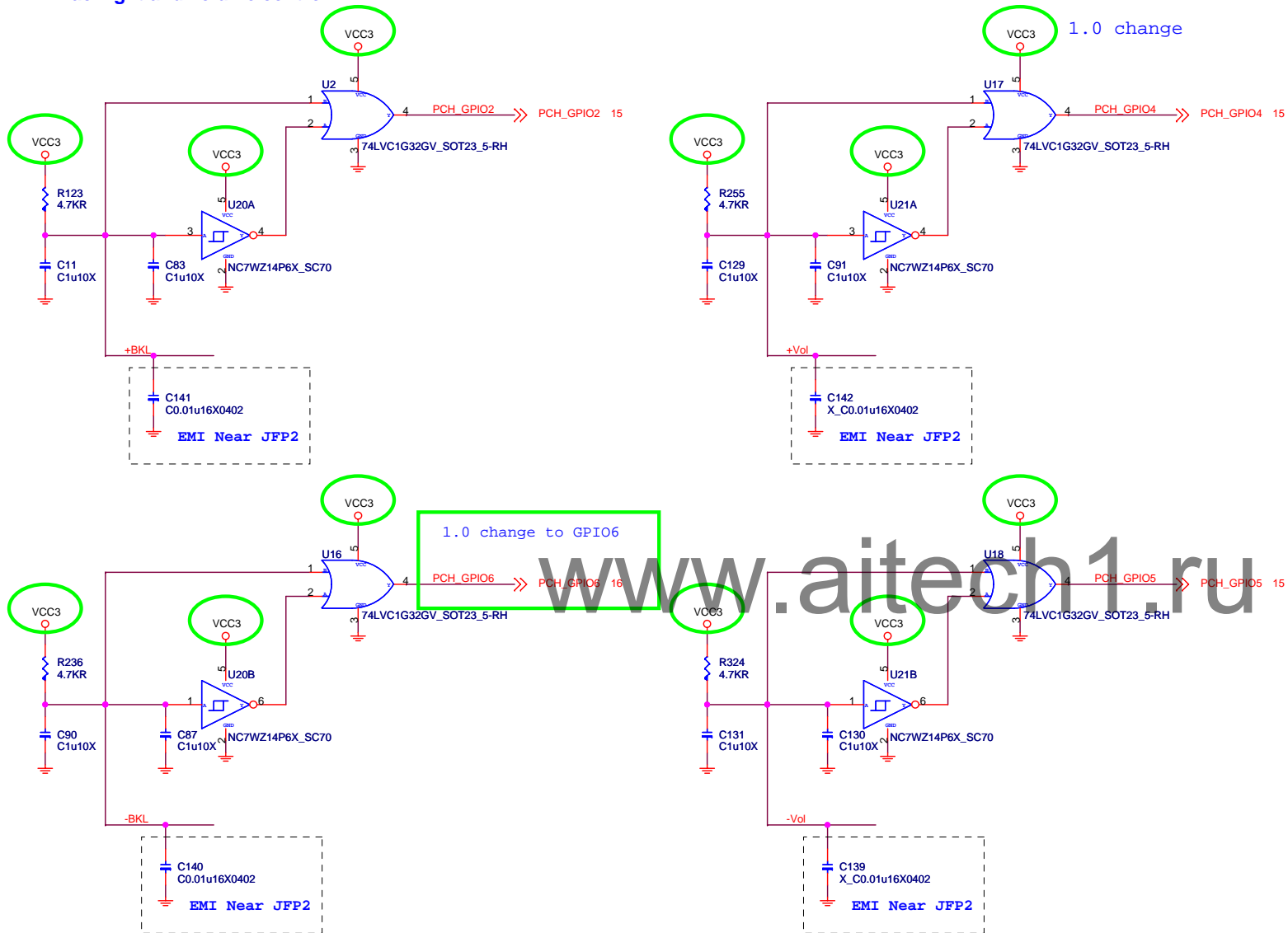


MICRO-STAR INT'L CO.,LTD

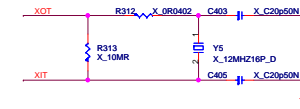
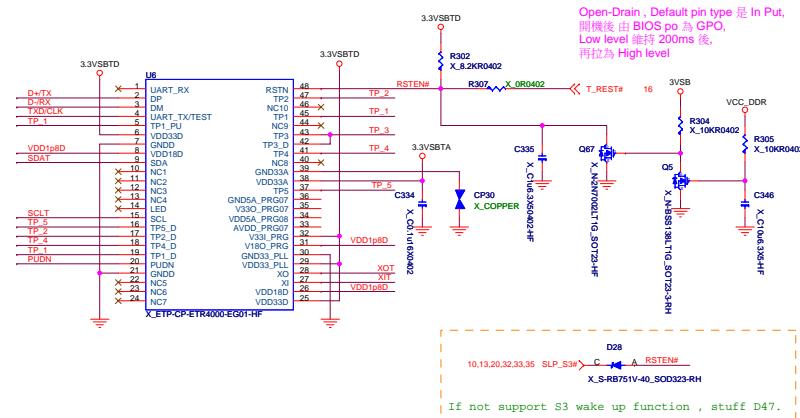
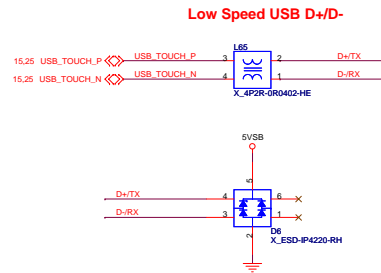
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Backlight and volume control

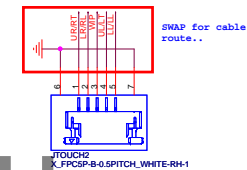
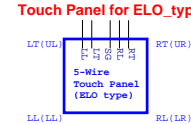


Non TOUCH NO STUFF

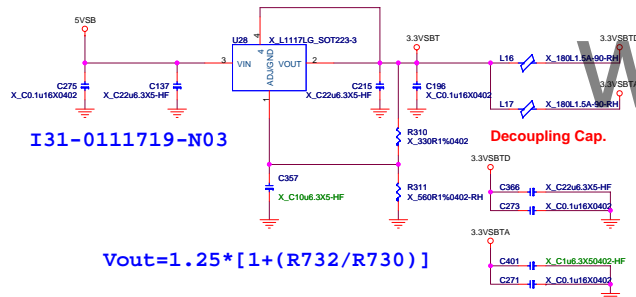


Touch Panel

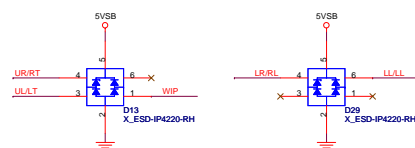
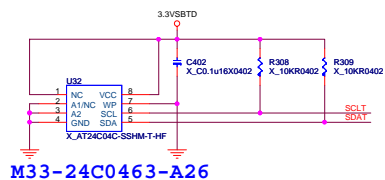
(Option 1) 5-Wire Resistive



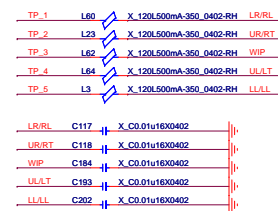
N5A-05F0020-H06

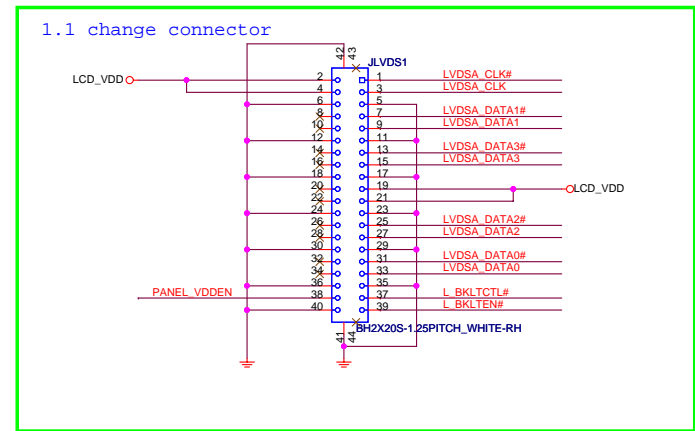
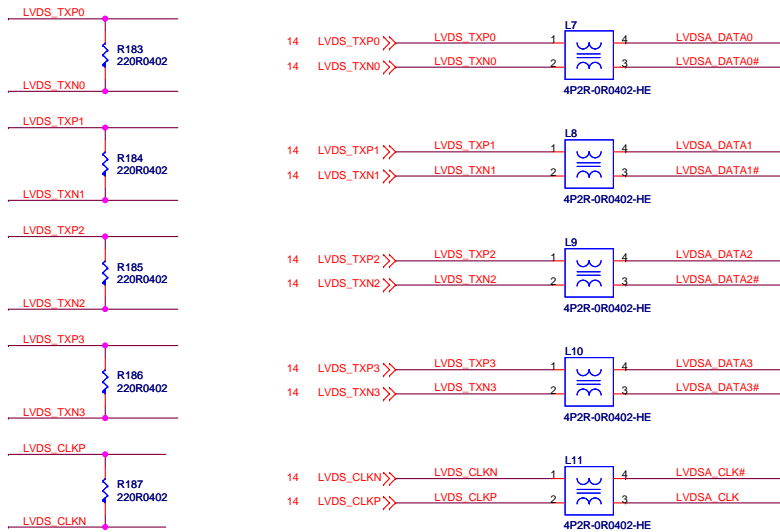


$$V_{out} = 1.25 * [1 + (R_{732}/R_{730})]$$

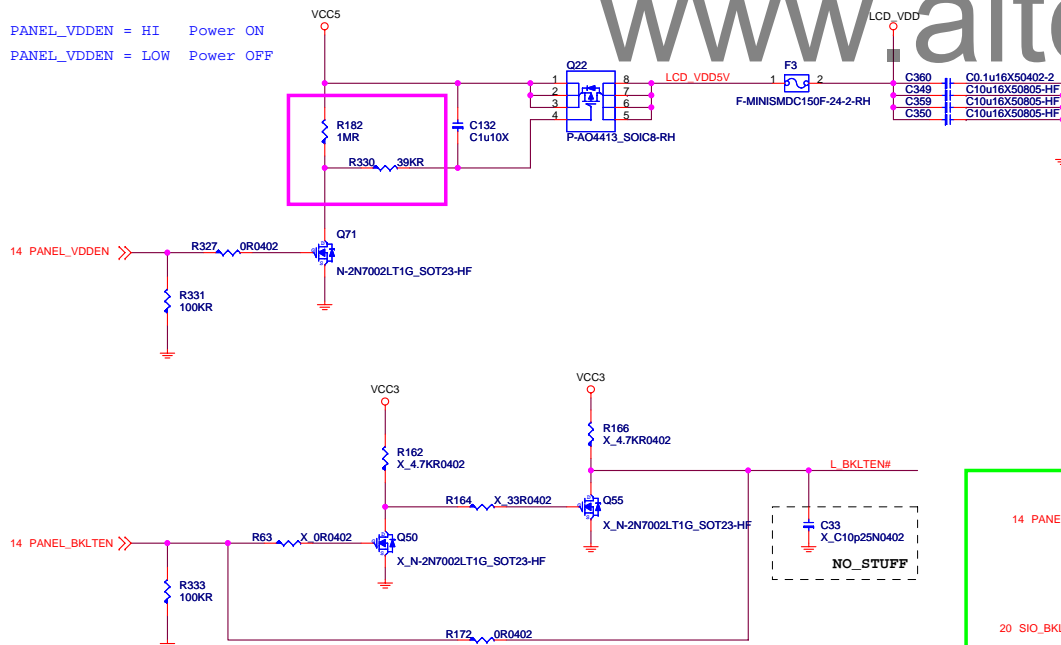


EMI Suppressor

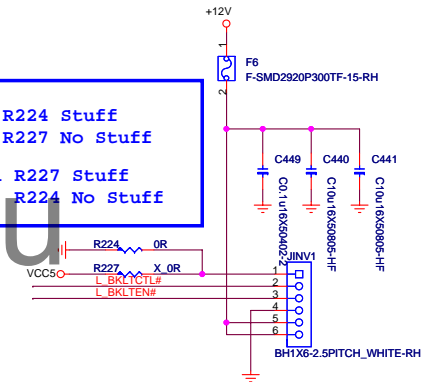




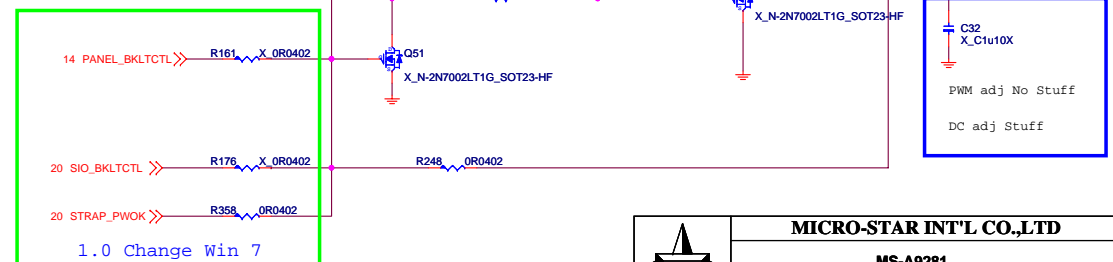
PANEL_VDDEN = HI Power ON
PANEL_VDDEN = LOW Power OFF



LED Panel R224 Stuff
R227 No Stuff
CCFL Panel R227 Stuff
R224 No Stuff

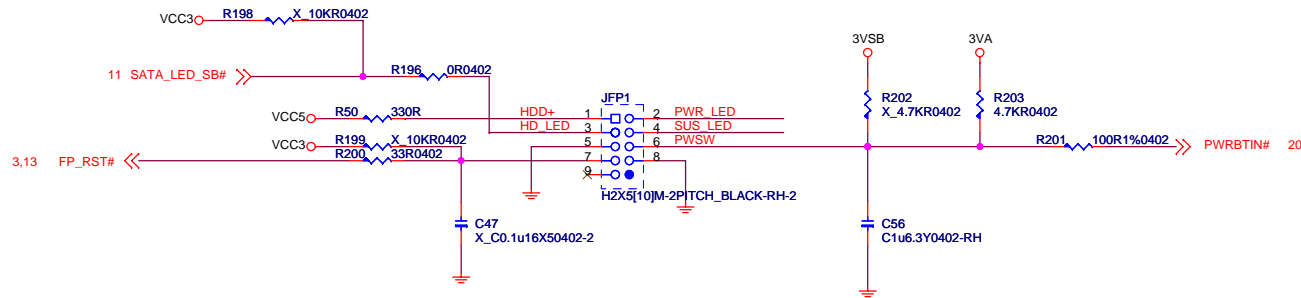


N32-1060511-H06

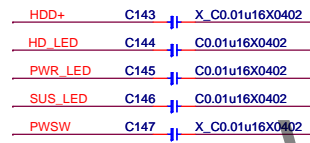


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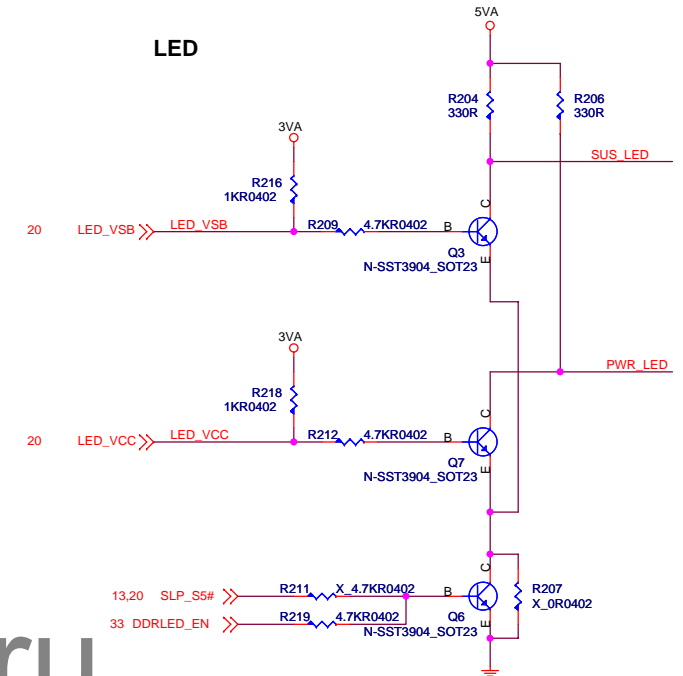
INTEL/PB Front Panel Connector



EMI Near JFP2

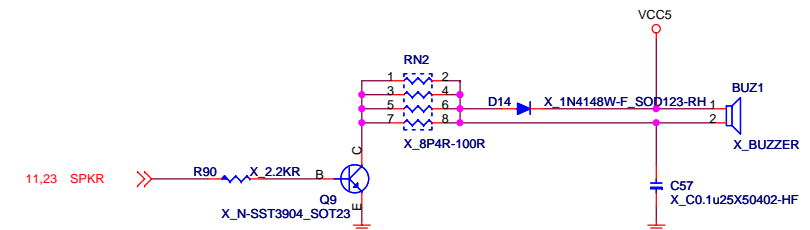


LED



1.0 COST DOWN

SPEAKER BLOCK

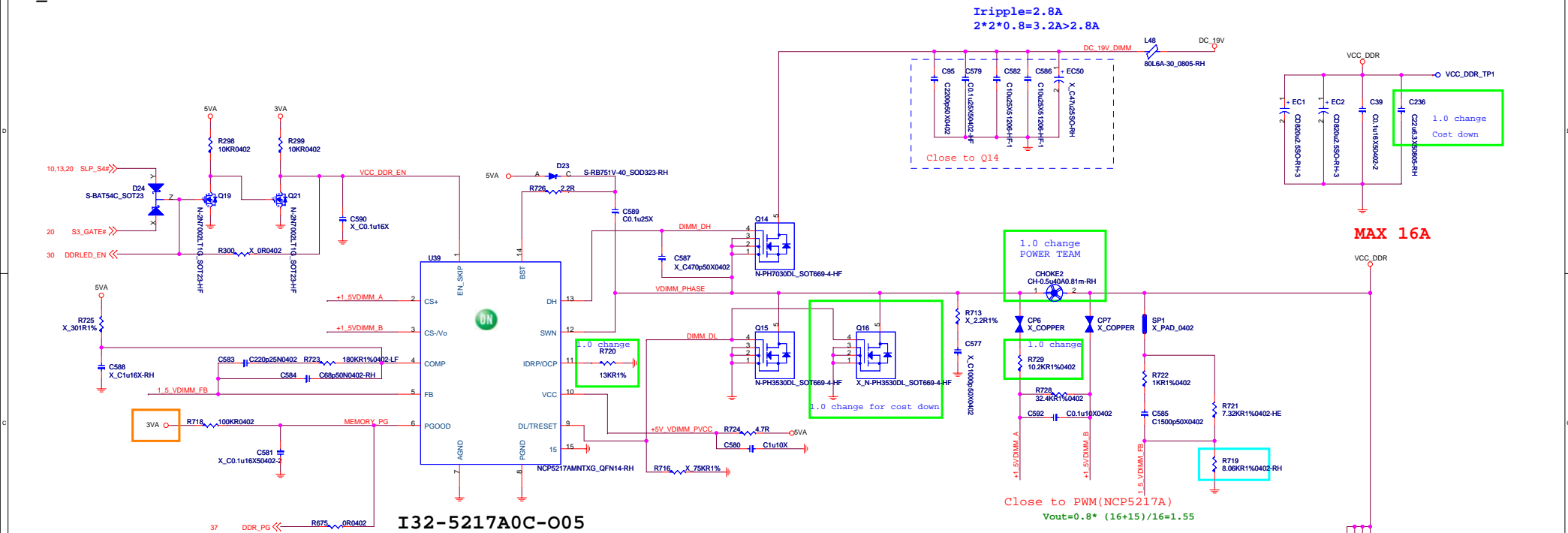


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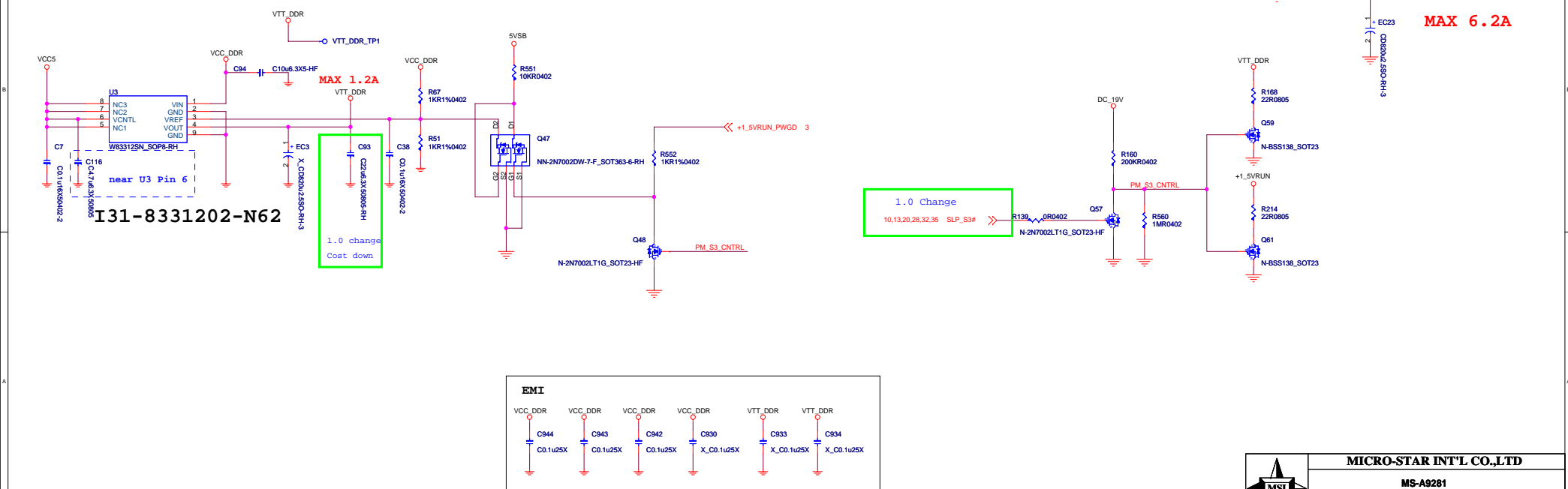
Size	Document Description	Rev
Custom	Front Panel	1.1
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VCC DDR

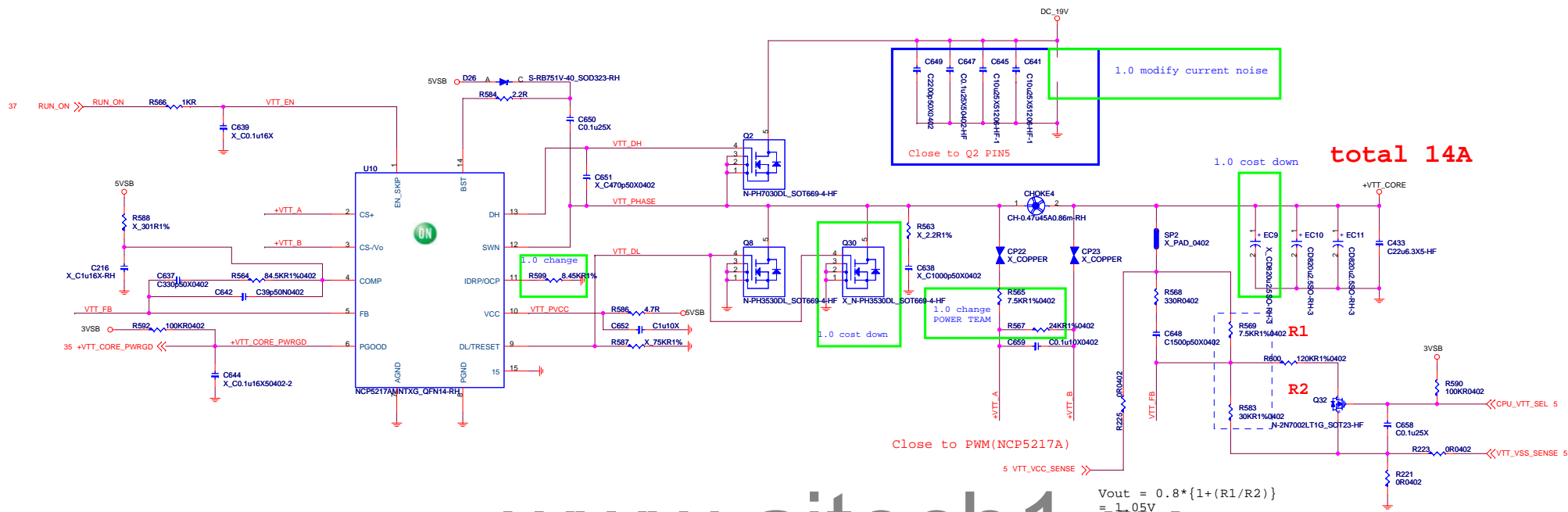


VTT_DDR

Current Max at 1.2A



+VTT_CORE Power



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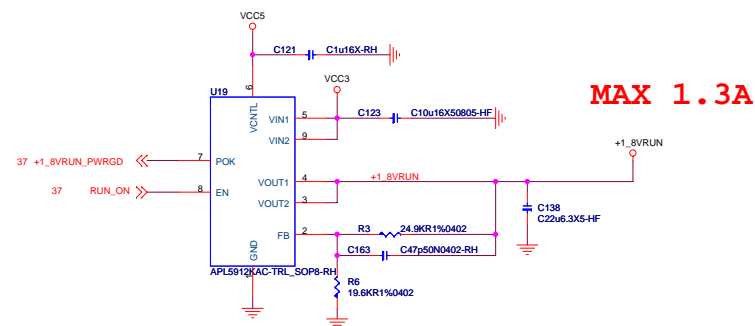
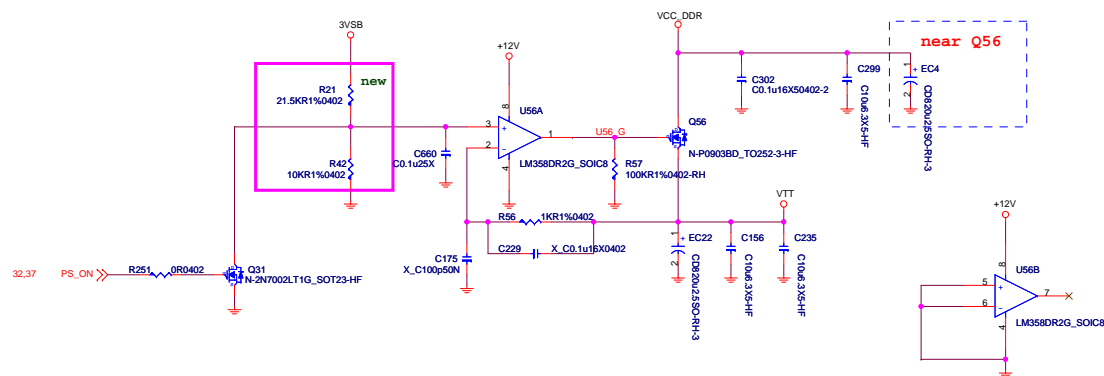
MAX 6.8A

VTT=1.05 V TO PCH

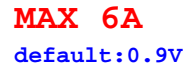
H_VTTVID1=LOW, 1.0V

H_VTTVID1=HIGH, 1.05V

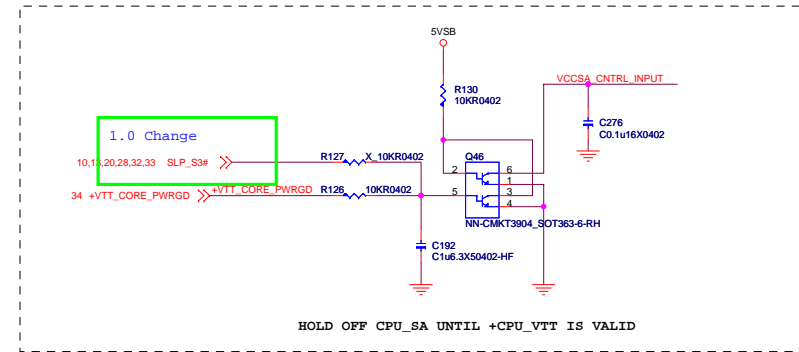
VTT POWER



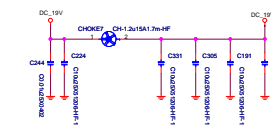
(8 . 8A)



Max current : 6A



VCCSA_VID=LOW, 0.9V
VCCSA_VID=HIGH, 0.85V



VCCP

VCCP_TPI

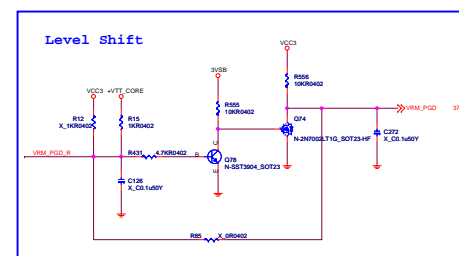
EC19 1* \parallel CD820v2.550-RH-3

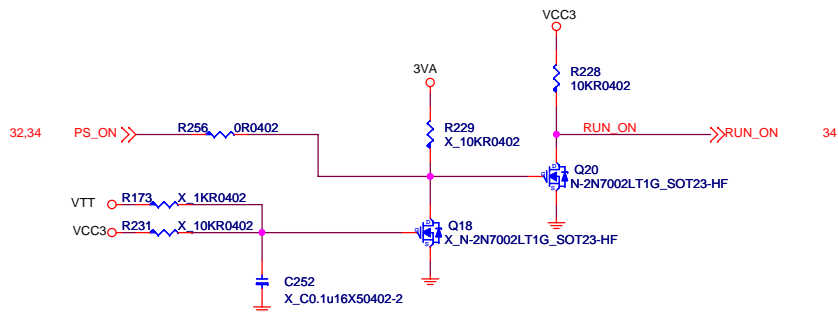
EC18 1* \parallel X_CD820v2.550-RH-3 1.0 cost down

EC20 1* \parallel CD820v2.550-RH-3

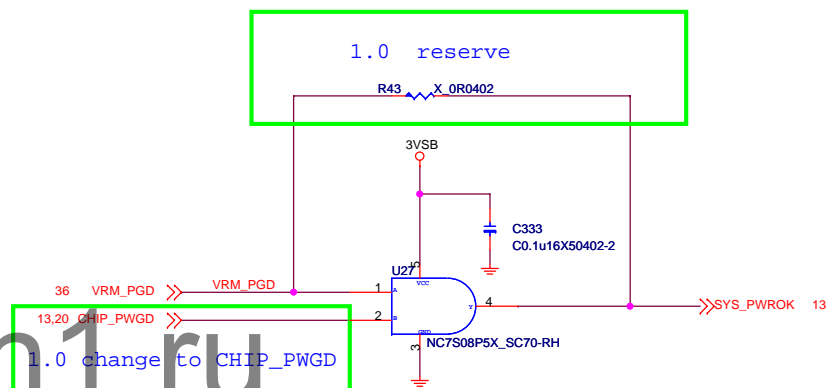
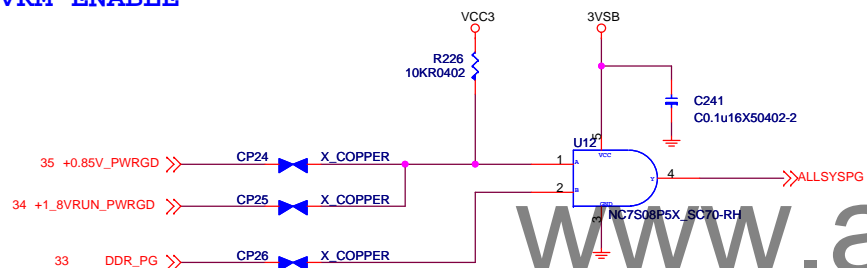
EC21 1* \parallel CD820v2.550-RH-3

The diagram illustrates the transition from a single CPU GFX bus to a split bus configuration. On the left, the 1.0 version shows a single bus with a red arrow pointing down labeled "1.0 cost down". On the right, the 0805 version shows a split bus with a red arrow pointing down labeled "1.0 change to 0805". The split bus configuration includes components labeled C814, C815, C812, and C813, along with a "SOT" label.





VRM ENABLE

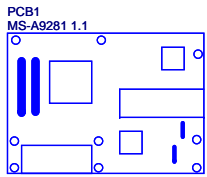


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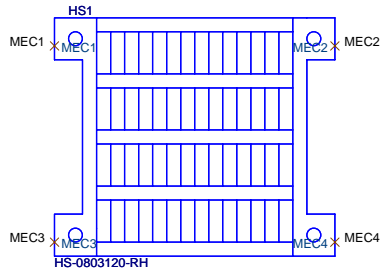
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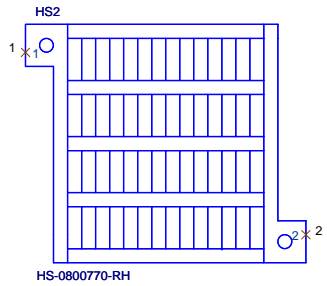


LABEL1
RESISTER
BIOS LABEL

CPU Sink



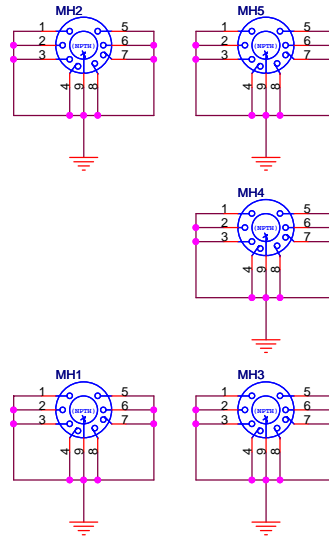
PCH Sink



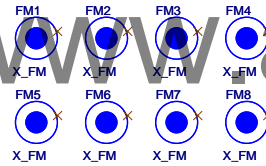
Single End 50ohm



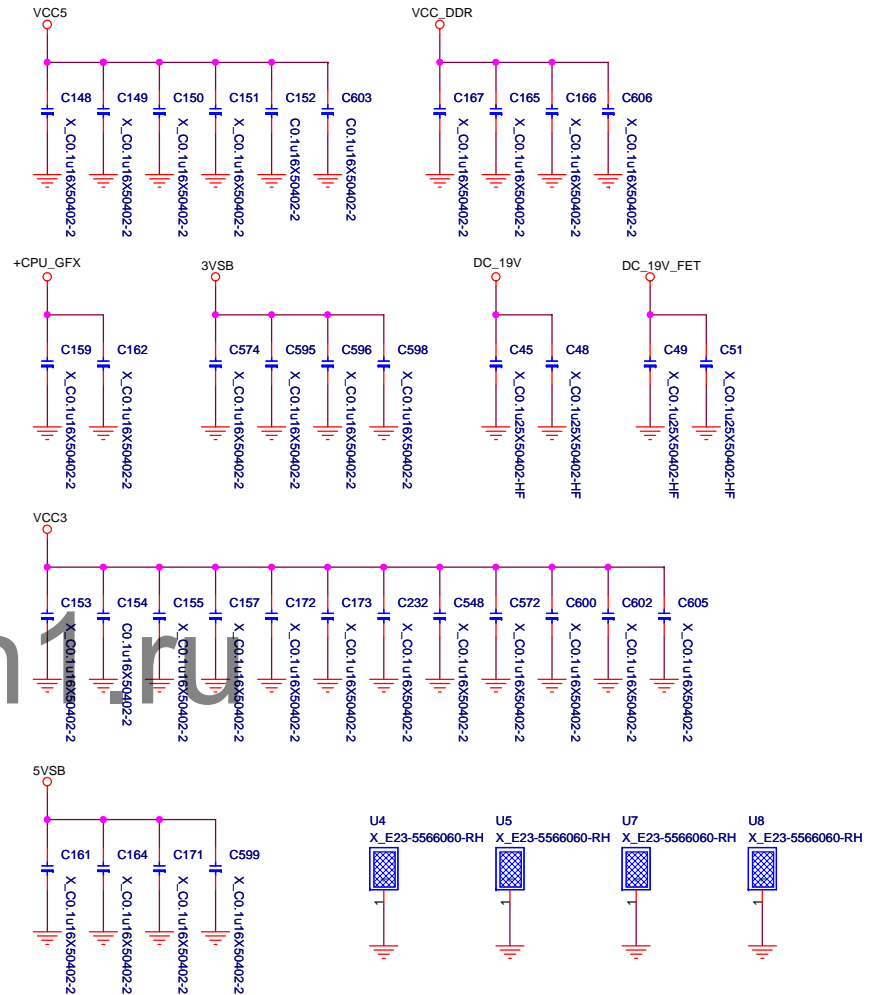
Mounting Holes



Optical Fiducial Marks-120



EMI

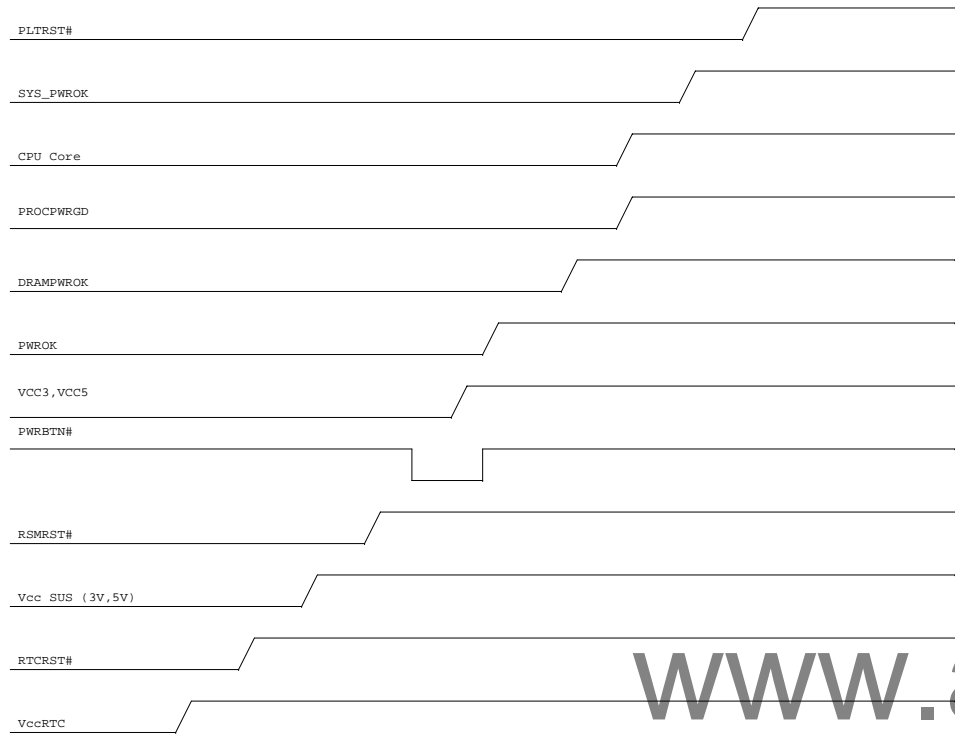


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Power Sequencing and Reset Signal Timings (NM 70)



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Celeron 847 (17W)		
Vcc CORE	-	33A
Vcc IO 1.05V	-	8.5A
VDDQ 1.5V	-	5A
Vcc SA 0.85V	-	6A
Vcc PLL 1.8V	-	1.2A
VAXG	-	16A

NM 70		
VccCore	-	1.73A
VccIO 1.05V	-	3.799A
VccASW 6704 mA	-	0.803A
Other	-	0.372A
Vcc3_3	-	0.252A
VccSus3_3	-	0.076A
+1_8VRUN 1.8V	-	0.042A
VccVRM 1.5V	-	0.147A
V5REF	-	0.001A
V5REF_Sus	-	0.001A
VccRTC	-	6uA

REALTEK/RTL8111E-VB		
3VSB -> VDD3	-	0.17A

HD Audio ALC887		
VCC3	-	0.012A
5VSB -> LDOVDD	-	0.05A

AMP TPA2008		
VCC5 -> PVCC	-	1.5A

NCP6151/6131		
VCCP	0.3V-1.52V	33A
+CPU_GFX	0V-1.52V	16A

NCP5217		
VCC_DDR	1.5V	16A

W83312SN		
VTT_DDR	0.75V	1.2A

N-AO4468		
+1_5VRUN	1.5V	6.147A

NCP5217		
+VTT_CORE	1.05V	14A

OP		
VTT	1.05V	6.704A

APL5912		
+1_8VRUN	1.8V	1.242A

N-AO4469		
+0.85VRUN	0.85V	6A

DDRIII x2 & TERMINATOR		
VTT_DDR	-	1.2A
VCC_DDR	-	4A

SATA HDD /SATA ODD		
VCC5	-	2A

(LVDS) LCD PANEL		
VCC5 -> LCD_VDD	-	1.5A
(IRUSH)	-	3A

USB 2.0 PORT X4		
5VSB -> SVCC1	-	2A
5VSB -> SVCC2	-	2A

Mini PCI-E slot x2		
VCC3	-	2.75A
3VSB	-	2.75A
1.5V -> +1_5VRUN	-	1A

Webcam		
	-	0.5A
Card Reader		
	-	0.3A

3V
Battery

TI/TPS51125	
VCC5	5.2A
VCC3	4.5A
5VSB	3A
+5VALW	0.5A
3VSB	3A
+3VALW	0.5A

+12V		
NCP1587DR2G	-	2A

+19V		
ADAPTER	-	

+12V CPU & SYS FAN		
	-	1A

INVETER		
	-	1A



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0A change to 1.0

- 01. Page 03 Modify TRMTRIP# circuit.
- 02. Page 03 DRAMRST# enable control pin change to SIO.
- 03. Page 05 Remove CAP & Change to 0805 for Cost down.
- 04. Page 06 Remove CAP for Cost down.
- 05. Page 09 DIMM1 CONNECTOR chang to N13-2040790-CK3 . (cost down)
- 06. Page 10 change USB3.0 connector.
- 07. Page 10 reserve C43 for USB3.0 controller loss.
- 08. Page 10 Modify USB3.0 SPI ROM circuit.
- 09. Page 10 reserve USB2.0 SKU circuit.
- 10. Page 11 Change PCH SPI ROM POWER to VCC3.
- 11. Page 12 reserve PCH GPIO26 to USB3.0/2.0 SKU detect.
- 12. Page 16 reserve PCH GPIO57 to FAN SKU detect.
- 13. Page 16 Modify PCH_GPIO36 Double pull high low R510 no stuff.
- 14. Page 20 Add about FAN circuit.
- 15. Page 20 Modify Temperature SENSING CIRCUIT.
- 16. Page 23 Change CAP to 0805 for Cost down.
- 17. Page 24 mini PCIE CONNECTOR change to N11-0520240-K06 . (cost down)
- 18. Page 25 Change CAP to 0805 for Cost down.
- 19. Page 26 Add FAN circuit.
- 20. Page 27 OSD POWER change to VCC3
- 21. Page 27 Modify OSD circuit. (GPIO3 change to GPIO6)
- 22. Page 29 Change LVDS Connector.
- 23. Page 29 Panel Backlight Brightness Control change to PCH.
- 24. Page 30 Remove SPEAKER for Cost down.
- 25. Page 31 Remove CAP for Cost down.
- 26. Page 32 Remove CAP & change to 0805 for Cost down.
- 27. Page 33 Modify VCC_DDR circuit. (POWER TEAM)
 CHOKE2 change to L04-05A7211-L65
 R720 change to 13K ohm
 R729 change to 10.2K ohm

 Change CAP to 0805 for Cost down.
 Remove Q16 for Cost down.

- 28. Page 34 Modify +VTT_CORE POWER circuit. (POWER TEAM)
 R565 change to 7.5 Kohm
 R567 change to 24K ohm
 R599 change to 8.45K ohm

 Remove Q30 & CAP for Cost down & current noise.
- 29. Page 36 Modify CPU POWER circuit. (POWER TEAM)
 R194 change to 1.82 Kohm
 R336 change to 3.9K ohm
 R36 change to 2.37K ohm
 R561 change to 2.37K ohm
 R54 change to 2.37K ohm

 Remove CAP & change to 0805 for Cost down & current noise.
- 30. Page 37 Modify ACPI circuit.
- 31. New BOM for Touch : Cfg-USB3T OPT:B 11/08
 Cfg-USB2T OPT:C

1.1 change to 1.1

- 01. Page 29 LVDS connector change to N32-2200120-H06.

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Panther Point (Mobile)

GPIO	Alt Func	Type	POWER	SMI	TOL	DEFAULT	SIGNAL NAME	Pull up or Pull down	BIOS
GPIO0	BMBUSY#	I/O	CORE	Y	3.3V	GPI	PCH_GPIO0	Pull-up 10K to VCC3	No USE
GPIO1	Unmultiplexed	I/O	CORE	Y	3.3V	GPI	NEC_SMB	Pull-up 10K to VCC3	USB3.0
GPIO2	PIRQE#	I/OD	CORE	Y	5V	GPI	PCH_GPIO2	Pull-up 8.2K to VCC3	+BKL
GPIO3	PIRQF#	I/OD	CORE	Y	5V	GPI	PCH_GPIO3	Pull-up 8.2K to VCC3	-BKL
GPIO4	PIRQG#	I/OD	CORE	Y	5V	GPI	PCH_GPIO4	Pull-up 8.2K to VCC3	+Vol
GPIO5	PIRQH#	I/OD	CORE	Y	5V	GPI	PCH_GPIO5	Pull-up 8.2K to VCC3	-Vol
GPIO6	Unmultiplexed	I/O	CORE	Y	3.3V	GPI	PCH_GPIO6	Pull-up 10K to VCC3	No USE
GPIO7	Unmultiplexed	I/O	CORE	Y	3.3V	GPI	PCH_GPIO7	Pull-up 10K to VCC3	No USE
GPIO8	Unmultiplexed	I/O	Suspend	Y	3.3V	GPO	ICC_EN	Pull-down	STRAP
GPIO9	OC5#	I/O	Suspend	Y	3.3V	Native	USB_OC5#	Pull-up 10K to 3VSB	OC5#
GPIO10	OC6#	I/O	Suspend	Y	3.3V	Native	USB_OC0#	Pull-up 10K to 3VSB	OC0#
GPIO11	SMBALERT#	I/O	Suspend	Y	3.3V	Native	PCH_GPIO11	Pull-up 10K to 3VSB	No USE
GPIO12	LAN_PHY_PWR_CTRL	I/O	Suspend	Y	3.3V	Native	T_REST#	N/A	T_REST#
GPIO13	HDA_DOCK_RST#	I/O	Suspend	Y	3.3V	GPI	SIO_PME#	Pull-up 4.7K to 3VSB	PME#
GPIO14	OC7#	I/O	Suspend	Y	3.3V	Native	NC	N/A	No USE
GPIO15	Unmultiplexed	I/O	Suspend	Y	3.3V	GPO	TLSEN	N/A	No USE
GPIO16	SATA4GP	I/O	CORE	N	3.3V	GPI	PCH_GPIO16	Pull-UP 10K to VCC3	No USE
GPIO17	Unmultiplexed	I/O	CORE	N	3.3V	GPI	PCH_GPIO17	Pull-up 10K to VCC3	No USE
GPIO18	PCIECLKRQ1#	I/O	CORE	N	3.3V	Native	PCIE_CLKREQ#1	Pull-up 10K to VCC3	PCIECLKREQ1#
GPIO19	SATA1GP	I/O	CORE	N	3.3V	GPI	BBS_BIT0	Pull-up 10K to VCC3	STRAP
GPIO20	PCIECLKRQ2# SMI#	I/O	CORE	N	3.3V	Native	PCIECLKREQ2#	Pull-up 10K to VCC3	PCIECLKREQ2#
GPIO21	SATA0GP	I/O	CORE	N	3.3V	GPI	PCH_GPIO21	Pull-up 10K to VCC3	No USE
GPIO22	SCLOCK	I/O	CORE	N	3.3V	GPI	PCH_GPIO22	Pull-up 10K to VCC3	No USE
GPIO23	LDRQ1#	I/O	CORE	N	3.3V	Native	NC	N/A	No USE
GPIO24	Unmultiplexed	I/O	Suspend	N	3.3V	GPO	PCH_GPIO24	Pull-up 10K to 3VSB	No USE
GPIO25	PCIECLKRQ3#	I/O	Suspend	N	3.3V	Native	PCIECLKREQ3#	Pull-up 10K to VCC3	PCIECLKREQ3#
GPIO26	PCIECLKRQ4#	I/O	Suspend	N	3.3V	Native	PCIECLKREQ4#	Pull-up 10K to 3VSB	No USE
GPIO27	Unmultiplexed	I/O	DSW	N	3.3V	GPI	DSW_WAKE#	Pull-up 10K to 3VA	No USE
GPIO28	Unmultiplexed	I/O	Suspend	N	3.3V	GPO	PLL_ODVR_EN	N/A	No USE
GPIO29	SLP_LAN#	I/O	Suspend	N	3.3V	Native	SLP_LAN#	Pull-up 10K to 3VSB	No USE
GPIO30	SUSWARN# SUSPWRDNACK	I/O	Suspend	N	3.3V	Native	SUS_WARN#	Pull-up 10K to 3VSB	SUS_WARN#
GPIO31	Unmultiplexed	I/O	DSW	N	3.3V	GPI	AC_PRESENT	Pull-up 10K to 3VA	No USE
GPIO32	only CLKRUN#	I/O	CORE	N	3.3V	Native	CLKRUN#	Pull-up 8.2K to VCC3	No USE
GPIO33	HDA_DOCK_EN#	I/O	CORE	N	3.3V	GPO	HDA_DOCK_EN#	N/A	No USE
GPIO34	STP_PCI#	I/O	CORE	N	3.3V	GPI	STP_PCI#	Pull-up 10K to VCC3	No USE
GPIO35	NMI#	I/O	CORE	N	3.3V	GPO	CAMERA_ON#	N/A	CAMERA_ON#
GPIO36	SATA2GP	I/O	CORE	N	3.3V	GPI	PCH_GPIO36	Pull-up 10K to VCC3	STRAP
GPIO37	SATA3GP	I/O	CORE	N	3.3V	GPI	PCH_GPIO37	Pull-down 10K	STRAP
GPIO38	SLOAD	I/O	CORE	N	3.3V	GPI	WLAN1_PWRON	Pull-up 10K to VCC3	WLAN1_PWRON
GPIO39	SDATAOUT0	I/O	CORE	N	3.3V	GPI	WLAN2_PWRON	Pull-up 10K to VCC3	WLAN2_PWRON
GPIO40	OC1#	I/O	Suspend	N	3.3V	Native	USB_OC0#	Pull-up 10K to 3VSB	OC0#
GPIO41	OC2#	I/O	Suspend	N	3.3V	Native	USB_OC0#	Pull-up 10K to 3VSB	OC0#
GPIO42	OC3#	I/O	Suspend	N	3.3V	Native	USB_OC0#	Pull-up 10K to 3VSB	OC0#
GPIO43	OC4#	I/O	Suspend	N	3.3V	Native	USB_OC4#	Pull-up about 3VSB	OC4#
GPIO44	PCIECLKRQ5#	I/O	Suspend	N	3.3V	Native	CHARGER_EN	Pull-up 10K to 3VSB	CHARGER_EN
GPIO45	PCIECLKRQ6#	I/O	Suspend	N	3.3V	Native	CHARGER_S0	Pull-up 10K to 3VSB	CHARGER_S0

GPIO	Alt Func	Type	POWER	SMI	TOL	DEFAULT	SIGNAL NAME	Pull up or Pull down	BIOS
GPIO46	PCIECLKRQ7#	I/O	Suspend	N	3.3V	Native	CHARGER_S1	Pull-up 10K to 3VSB	CHARGER_S1
GPIO47	PEG_A_CLKRQ#	I/O	Suspend	N	3.3V	Native	PEGACLKREQ#	Pull-up 10K to 3VSB	No USE
GPIO48	SDATAOUT1	I/O	CORE	N	3.3V	GPI	PCH_GPIO48	Pull-up 10K to VCC3	No USE
GPIO49	SATA5GP TEMP_ALERT#	I/O	CORE	N	3.3V	GPI	PCH_GPIO49	Pull-up 10K to VCC3	No USE
GPIO50	Unmultiplexed	I/O	CORE	N	5V	Native	PCH_GPIO50	Pull-up 8.2K to VCC3	No USE
GPIO51	Unmultiplexed	I/O	CORE	N	3.3V	Native	BBS_BIT1	N/A	STRAP
GPIO52	Unmultiplexed	I/O	CORE	N	5V	Native	PCH_GPIO52	Pull-up 8.2K to VCC3	No USE
GPIO53	Unmultiplexed	I/O	CORE	N	3.3V	Native	PCH_GPIO53	N/A	No USE
GPIO54	Unmultiplexed	I/O	CORE	N	5V	Native	PCH_GPIO54	Pull-up 10K to VCC3	No USE
GPIO55	Unmultiplexed	I/O	CORE	N	3.3V	Native	PCH_GPIO55	N/A	No USE
GPIO56	PEG_B_CLKRQ#	I/O	Suspend	N	3.3V	Native	PEGBCLKRQ#	Pull-up 10K to 3VSB	No USE
GPIO57	Unmultiplexed	I/O	Suspend	N	3.3V	GPI	PCH_GPIO57	Pull-up 10K to 3VSB	No USE
GPIO58	SML1CLK	I/O	Suspend	N	3.3V	Native	SML1_CLK	Pull-up 2.2K to 3VSB	SML1CLK
GPIO59	OC0#	I/O	Suspend	N	3.3V	Native	USB_OC0#	Pull-up 10K to 3VSB	OC0#
GPIO60	SML0ALERT#	I/O	Suspend	N	3.3V	Native	DRAMRST_CNTRL_PCH	Pull-up 10K to 3VSB	DEEP S3
GPIO61	SUS_SATA#	I/O	Suspend	N	3.3V	Native	NC	N/A	No USE
GPIO62	SUSCLK	I/O	Suspend	N	3.3V	Native	NC	N/A	No USE
GPIO63	SLP_S5#	I/O	Suspend	N	3.3V	Native	SLP_S5#	N/A	SLP_S5#
GPIO64	CLKOUTFLEX0	I/O	CORE	N	3.3V	Native	TP_CLK_FLEX0	N/A	No USE
GPIO65	CLKOUTFLEX1	I/O	CORE	N	3.3V	Native	TP_CLK_FLEX1	N/A	No USE
GPIO66	CLKOUTFLEX2	I/O	CORE	N	3.3V	Native	TP_CLK_FLEX2	N/A	No USE
GPIO67	CLKOUTFLEX3	I/O	CORE	N	3.3V	Native	TP_CLK_FLEX3	N/A	CLK_48M_SIO
GPIO68	Unmultiplexed	I/O	CORE	N	3.3V	GPI	NC	N/A	No USE
GPIO69	Unmultiplexed	I/O	CORE	N	3.3V	GPI	NC	N/A	No USE
GPIO70	Unmultiplexed	I/O	CORE	N	3.3V	Native	NC	N/A	No USE
GPIO71	Unmultiplexed	I/O	CORE	N	3.3V	Native	NC	N/A	No USE
GPIO72	BATLOW#	I/O	Suspend	N	3.3V	Native	PCH_GPIO72	Pull-up 10K to 3VSB	No USE
GPIO73	PCIECLKRQ0#	I/O	Suspend	N	3.3V	Native	LANCLKRQ#	Pull-up 10K to 3VSB	LANCLKRQ#
GPIO74	SML1ALERT# PCHHOT#	I/O	Suspend	N	3.3V	Native	PCH_GPIO74	Pull-up 10K to 3VSB	No USE
GPIO75	SML1DATA	I/O	Suspend	N	3.3V	Native	SML1_DATA	Pull-up 2.2K to 3VSB	SML1DATA



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